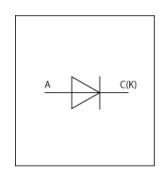


# LTspice Model ESD Protection Diode VISHAY P6SMB24CAHE3



## **Model Information**

Model A macro model based on general SPICE diode model

Call Name MDC\_P6SMB24CAHE3\_LT

Pin Assign 1:A 2:C

File List Model Library MDC\_P6SMB24CAHE3\_LT01.lib

Model Report MDC\_P6SMB24CAHE3\_LT.pdf (this file)

Verified Simulator Version LT

Note

LTspice version XVII

#### References

The information which was used for modeling is as follow:

[Data Sheet]

Date/Version 23-Dec-14

Product name
P6SMB24CAHE3

Company nameCharacteristicsVishay Intertechnology, Inc.BvTemp,IrVr(Breakdown)

#### Simulation Range

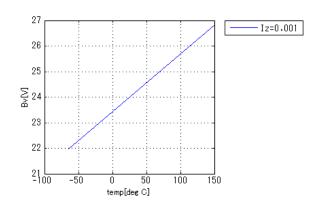
This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

Item	Range			Unit
	Min.		Max.	
Reverse Voltage	0	to	48	V
Temperature	-65	to	150	deg C

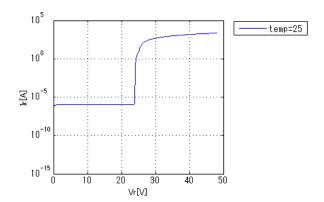


Simulation results are following. Explanatory notes — : simulated

## **BvTemp**



## IrVr(Breakdown)





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