

PSpice Model

3 Phase Driver IC

Infineon

TLE7183QU

Model Information

Model An original macro model
Call Name MDC_TLE7183QU_PS
Pin Assign See the "Notes"
File List Model Library MDC_TLE7183QU_PS01.lib
 Model Report MDC_TLE7183QU_PS.pdf (this file)

Verified Simulator Version PSpice version 16.6

Note

[Pin assign] 1:SL3 2:GND 3:VO 4:ISN 5:ISP 6:VRI 7:VRO 8:AGND 9:VTHOC 10:TP 11:DT 12:INH 13:IL3 14:IH3 15:U_FB 16:V_FB 17:W_FB 18:ENA1 19:ENA2 20:GND 21:IL1 22:IH1 23:IL2 24:IH2 25:ERR1 26:ERR2 27:CH2 28:CH1 29:CL1 30:VS 31:CL2 32:GND 33:CB1 34:VDH 35:GL1 36:SL1 37:GND 38:SH1 39:GH1 40:CB2 41:GL2 42:SL2 43:GH2 44:SH2 45:GH3 46:SH3 47:GND 48:GL3 49:EPAD

References

The information which was used for modeling is as follow:

[Data Sheet]

- Date/Version Unknown
- Product name TLE7183QU
- Company name Infineon Technologies AG
- Characteristics Transient

Simulation Condition

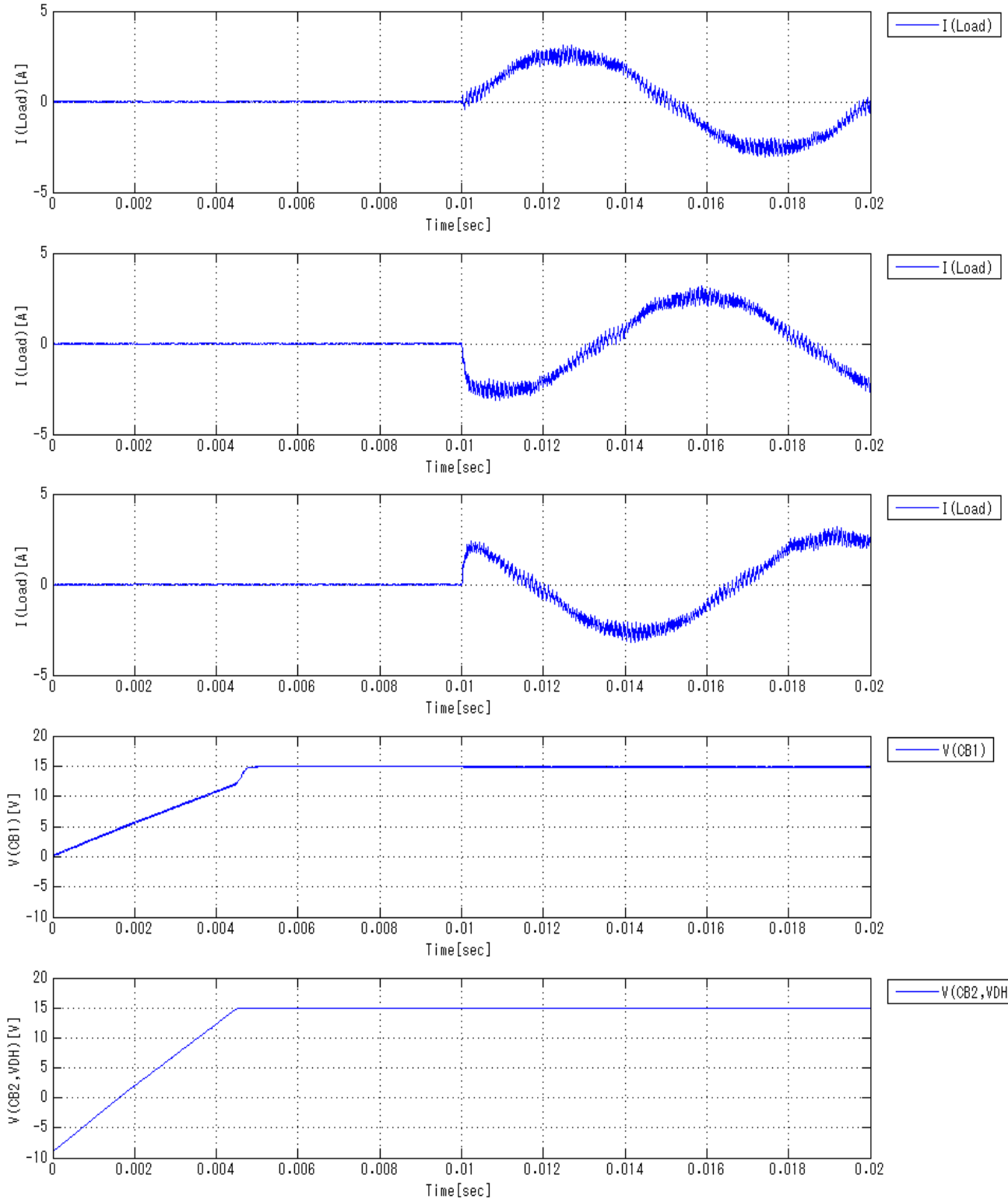
This table shows the value of evaluated simulation condition that was not occurs any convergence problems in this area.

Item	Condition	Unit
Temperature	25	deg C

Simulation results are following.
Explanatory notes — : simulated

Transient

Temp. = 25deg C



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MoDeCH Inc.

Head Office

Location: Mitsuseimei Hachioji Bldg., 5-15 Yokoyama-cho, Hachioji-Shi, Tokyo 192-0081, Japan

Tel:+81-42-656-3360

E-Mail: model-on-support@modech.co.jp

URL: <http://www.modech.com/en/>