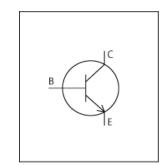


LTspice Model NPN ON FJD3305H1



Model Information

Model Mextram model
Call Name MDC_FJD3305H1_LT

Pin Assign 1:C 2:B 3:E
File List Model Library

Model Library MDC_FJD3305H1_LT01.lib MOC_FJD3305H1_LT.pdf (this file)

Verified Simulator Version

Note

LTspice version XVII

References

The information which was used for modeling is as follow:

[Data Sheet]

Date/Version Rev. A1Product name FJD3305H1

Company nameON Semiconductor.

● Characteristics IcVce[ib],hFEIc[Temp],Vce(sat)Ic[Temp],Vbe(sat)Ic[Temp],Ci

b,Cob

Simulation Range

This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

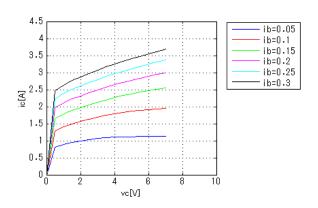
Item	Range			Unit
	Min.		Max.	
Collector-emitter voltage (DC)	0	to	400	V
Base current (DC)	0	to	2	Α
Temperature	-65	to	150	deg C



Simulation results are following. Explanatory notes — : simulated

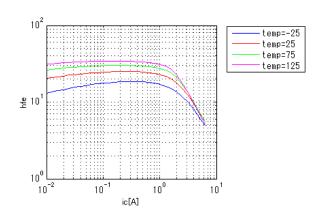
IcVce[ib]

Temp. = 25deg C



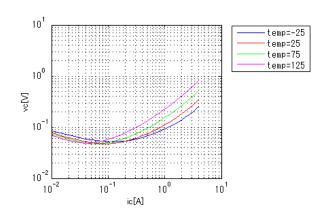
hFEIc[Temp]

Vce = 5V



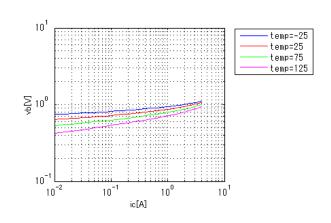
Vce(sat)lc[Temp]

IC/IB = 4



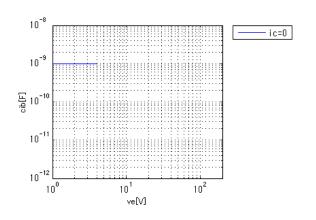
Vbe(sat)lc[Temp]

IC/IB = 4



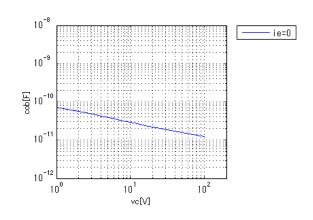
Cib

Freq. = 1MHz



Cob

Freq. = 1MHz





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