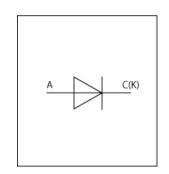


LTspice Model Rectifier Diode VISHAY POLYTECH SE80PWJHM3J



Model Information

Model A macro model based on general SPICE diode model

Call Name MDC SE80PWJHM3J LT

Pin Assign 1:A 2:C

File List Model Library MDC_SE80PWJHM3J_LT01.lib

Model Report MDC_SE80PWJHM3J_LT.pdf (this file)

Verified Simulator Version

Note

LTspice version XVII

References

The information which was used for modeling is as follow:

[Data Sheet]

Date/VersionProduct name29-Jul-2020SE80PWJHM3J

●Company name Vishay Polytech Co., Ltd.

● Characteristics IrVr[Temp],CjVr,IfVf[Temp],TrrIfIr,TrrWaveform,SurgeWavef

orm

Simulation Range

This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

Item	Range			Unit
	Min.		Max.	
Reverse Voltage	0	to	660	V
Temperature	-40	to	175	deg C



Model Functions Table

Diode

O: Implemented

×: Not Implemented

—: Not applicable

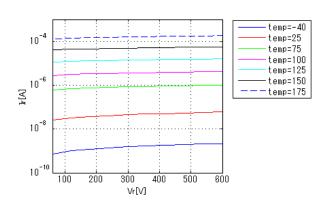
RANK=1

Functions	RANK	Implemented
IF-VF(Temp)	1	0
IR-VR(Temp)	1	0
Capacitance	1	0
Reverse recovery characteristics	1	0
Rectification characteristics(Bridge)	1	1
Surge forward current-Transient	1	0



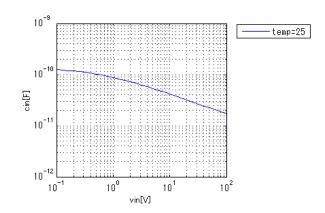
Simulation results are following. Explanatory notes — : simulated

IrVr[Temp]

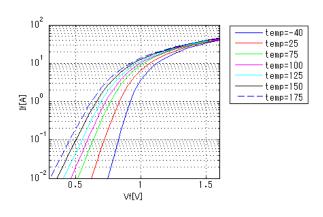


CjVr

Freq. = 1MHz

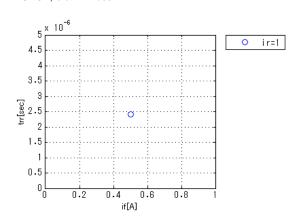


IfVf[Temp]



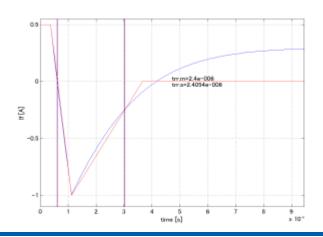
Trrlflr

irr = 0.25A, didt = 2A/us



Trr Waveform (Red: Datasheet Blue: Simulation)

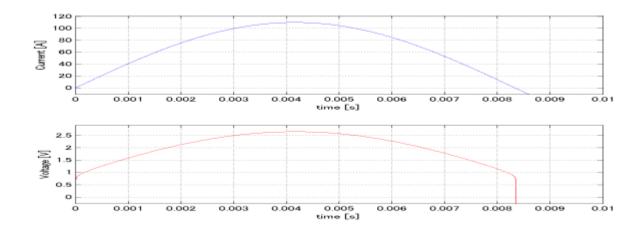
If = 0.5A, ir = 1A, irr = 0.25A, didt = 5A/us





Simulation results are following. Explanatory notes — : simulated

Surge Forward Current Waveform





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