

MDC_2N5551_PS

PSpice Model NPN JCET 2N5551

Model Information

ModelGummel-Poon modelCall NameMDC_2N5551_PSPin Assign1:C 2:B 3:EFile ListModel Library
Model Report

MDC_2N5551_PS02.lib MDC_2N5551_PS.pdf (this file)

Verified Simulator Version Note

PSpice version 16.6

References

The information which was used for modeling is as follow:

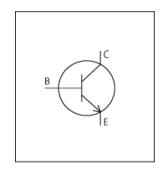
[Data Sheet]
Date/Version
Product name
Company name
Characteristics

E,Aug,2017 2N5551 Jiangsu Changjiang Electronics Technology Co., Ltd. IcVce[ib],hFEIc[Temp],Vbe(sat)Ic[Temp],Vce(sat)Ic[Temp],Ic Vbe[Temp],Cib,Cob,fTIc[Vce]

Simulation Range

This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

Item	Range			Unit
	Min.		Max.	
Collector-emitter voltage (DC)	0	to	160	V
Base current (DC)	0	to	0.06	А
Temperature	-55	to	150	deg C

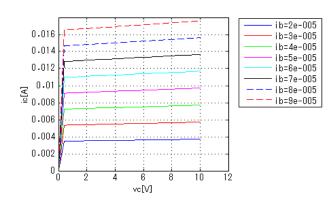




Simulation results are following. Explanatory notes -: simulated

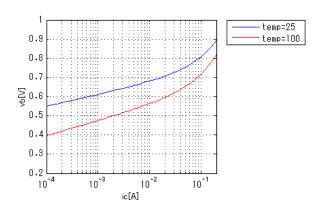
IcVce[ib]

Temp. = 25deg C



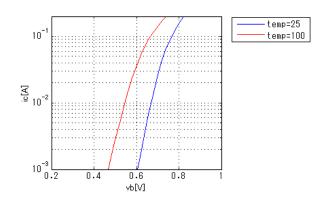
Vbe(sat)Ic[Temp]

IC/IB = 10



IcVbe[Temp]

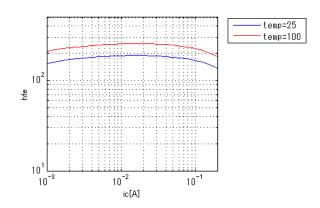
Vce = 5V



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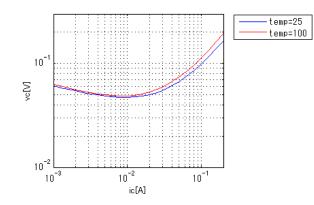
hFElc[Temp]

Vce = 5V

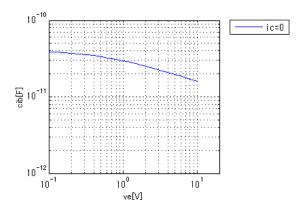


Vce(sat)lc[Temp]

IC/IB = 10



Cib Freq. = 1MHz

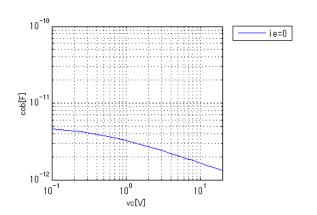




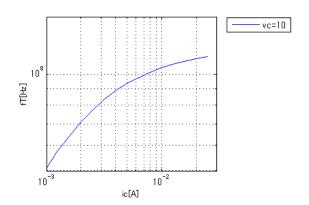
Simulation results are following. Explanatory notes -: simulated







fTIc[Vce]





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