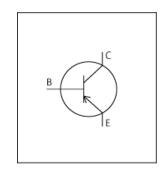


# PSpice Model PNP Panasonic DSA2001



# **Model Information**

Model Gummel-Poon model
Call Name MDC\_DSA2001\_PS

Pin Assign 1:C 2:B 3:E
File List Model Library

Model Library MDC\_DSA2001\_PS02.lib
Model Report MDC\_DSA2001\_PS.pdf (this file)

**Verified Simulator Version** 

Note

PSpice version 16.6

#### References

The information which was used for modeling is as follow:

[Data Sheet]

Date/VersionProduct nameVer. DJDDSA2001

Company namePanasonic Corporation

● Characteristics IcVce[ib],hFEIc[Temp],Vce(sat)Ic[Temp],IcVbe[Temp],Cob,fT

Ic[Vce]

#### **Simulation Range**

This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

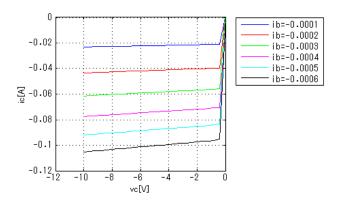
Item	Range			Unit
	Min.		Max.	
Collector-emitter voltage (DC)	0	to	-50	V
Base current (DC)	0	to	-10	Α
Temperature	-55	to	150	deg C



Simulation results are following. Explanatory notes — : simulated

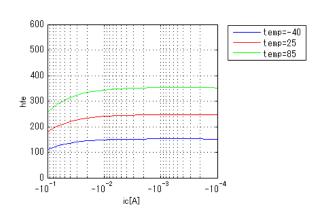
#### IcVce[ib]

Temp. = 25deg C



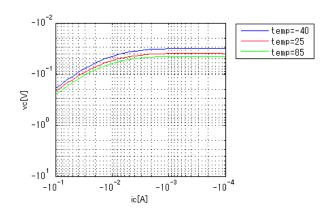
# hFEIc[Temp]

Vce = -10V



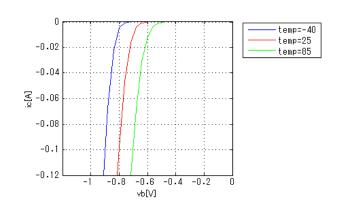
# Vce(sat)lc[Temp]

IC/IB = 10



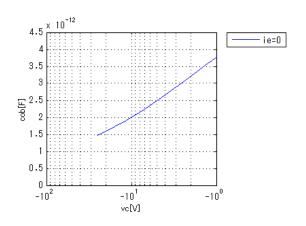
## IcVbe[Temp]

Vce = -10V

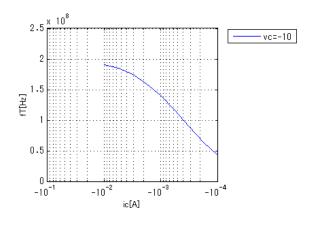


## Cob

Freq. = 1MHz



# fTlc[Vce]





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