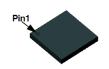
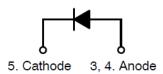


PSpice Model Schottky Diode (SiC) ON FFSM0665A







Schottky Diode

Model Information

Model A macro model based on general SPICE diode model

Call Name MDC_FFSM0665A_PS

Pin Assign 3:A 4:A 5:C

File List Model Library MDC_FFSM0665A_PS01.lib

Model Report MDC_FFSM0665A_PS.pdf (this file)

Verified Simulator Version

Note

PSpice version 17.2

References

The information which was used for modeling is as follow:

[Data Sheet]

Date/VersionDecember, 2019 - Rev. 4

Product nameCompany nameFFSM0665AON Semiconductor.

● Characteristics If Vf[Temp], Ir Vr[Temp], Cj Vr, Surge Current Waveform

Simulation Range

This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

Item	Range			Unit
	Min.		Max.	
Reverse voltage (DC)	0	to	650	٧
Temperature	-55	to	150	deg C



Model Functions Table

Diode

O:Implemented

×: Not Implemented

—: Not applicable

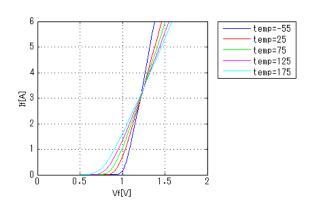
RANK=1

Functions	RANK	Implemented
IF-VF(Temp)	1	0
IR-VR(Temp)	1	0
Capacitance	1	0
Reverse recovery characteristics	1	_
Zz-Iz	1	_
Rectification characteristics(Bridge)	1	_
Surge current-Transient	1	0

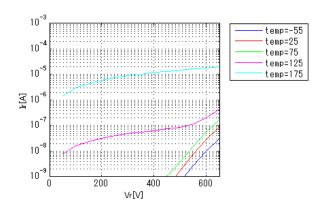


Simulation results are following. Explanatory notes — : simulated

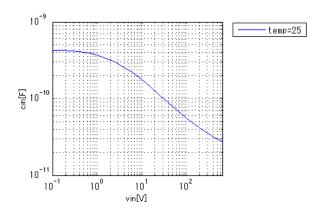
IfVf[Temp]



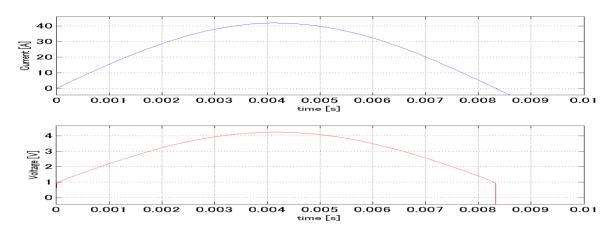
IrVr[Temp]



CjVr Freq = 1000000Hz



Surge Current Waveform (Forward Sine Half 60Hz)





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