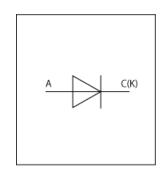


PSpice Model ESD Protection Diode VISHAY P4SMA75AHE3



Model Information

Model A macro model based on general SPICE diode model

Call Name MDC P4SMA75AHE3 PS

Pin Assign 1:A 2:C

File List Model Library MDC_P4SMA75AHE3_PS01.lib

Model Report MDC_P4SMA75AHE3_PS.pdf (this file)

Verified Simulator Version PSpice version 16.6

Note

References

The information which was used for modeling is as follow:

[Data Sheet]

Date/VersionProduct name06-Feb-15P4SMA75AHE3

Company nameCharacteristicsVishay Intertechnology, Inc.BvTemp,IrVr(Breakdown)

Simulation Range

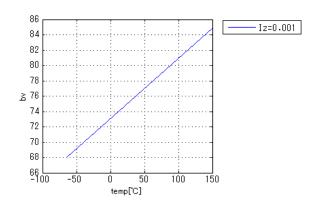
This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

| Item | Range | | | Unit |
|-----------------|-------|----|-------|-------|
| | Min. | | Max. | |
| Reverse Voltage | 0 | to | 150.1 | V |
| Temperature | -65 | to | 150 | deg C |

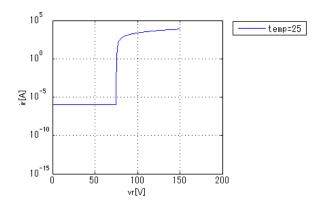


Simulation results are following. Explanatory notes — : simulated

BvTemp



IrVr(Breakdown)





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MoDeCH Inc.

Head Office

Location: Mitsuiseimei Hachioji Bldg., 5-15 Yokoyama-cho, Hachioji-Shi, Tokyo 192-0081, Japan

Tel:+81-42-656-3360

E-Mail:model-on-support@modech.co.jp

URL:http://www.modech.com/en/