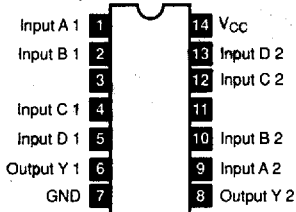


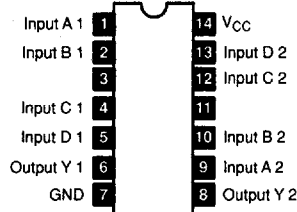
# INTEGRATED CIRCUITS – HTL SERIES

## (HIGH THRESHOLD LOGIC)

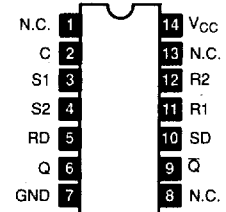
**NTE9660** 14-Lead DIP, See Diag. 247  
Expandable, Dual 4-Input NAND Gate



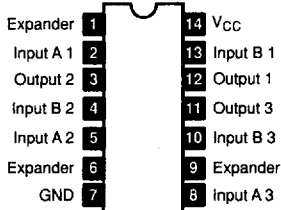
**NTE9661** 14-Lead DIP, See Diag. 247  
Expandable Dual 4-Input NAND Gate



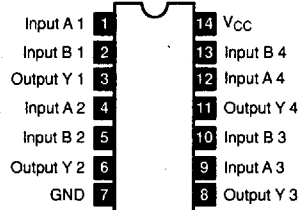
**NTE9664** 14-Lead DIP, See Diag. 247  
Master/Slave R/S Flip-Flop



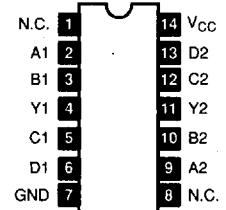
**NTE9666** 14-Lead DIP, See Diag. 247  
Triple Level Transistor



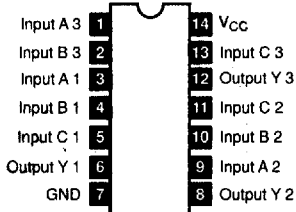
**NTE9668** 14-Lead DIP, See Diag. 247  
Quad 2-Input NAND Gate



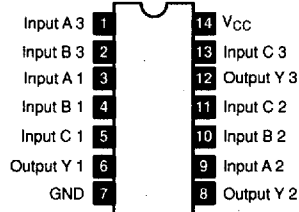
**NTE9669** 14-Lead DIP, See Diag. 247  
Dual 4-Input Expander



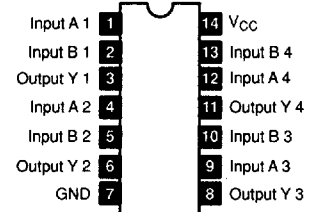
**NTE9670** 14-Lead DIP, See Diag. 247  
Triple 3-Input NAND Gate



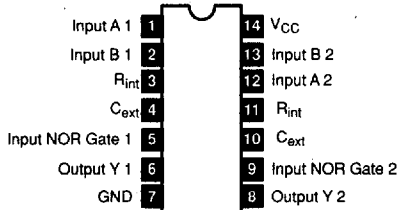
**NTE9671** 14-Lead DIP, See Diag. 247  
Triple 3-Input NAND Gate



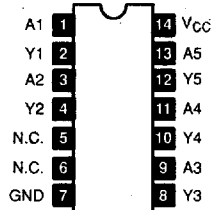
**NTE9672** 14-Lead DIP, See Diag. 247  
Quad 2-Input NAND Gate



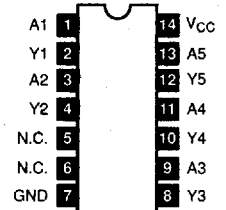
**NTE9675** 14-Lead DIP, See Diag. 247  
Dual Pulse Stretcher



**NTE9680** 14-Lead DIP, See Diag. 247  
Hex Inverter



**NTE9681** 14-Lead DIP, See Diag. 247  
Hex Inverter



See Diagrams, beginning on Page 1-293