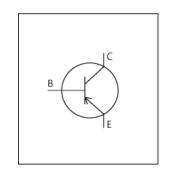


# LTspice Model PNP TOSHIBA 2SA2142



# **Model Information**

Model Gummel-Poon model Call Name MDC\_2SA2142\_LT

Pin Assign 1:B 2:C 3:E
File List Model Library

Model Library MDC\_2SA2142\_LT01.lib Model Report MDC\_2SA2142\_LT.pdf (this file)

**Verified Simulator Version** 

Note

LTspice version XVII

#### References

The information which was used for modeling is as follow:

[Data Sheet]

Date/VersionProduct name2013-11-012SA2142

Company name Toshiba Corporation

● Characteristics IcVce[ib],hFEIc[Temp],Vce(sat)Ic[Temp],Vbe(sat)Ic[Temp],Ic

Vbe[Temp],fTlc[Vce],CobVcb,SwitchingRl[Tname]

#### Simulation Range

This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

Item	Range			Unit
	Min.		Max.	
Collector-emitter voltage (DC)	0	to	-600	V
Temperature	-55	to	150	deg C



BJT

O: Implemented

×: Not Implemented

—: Not applicable

Model Functions Table	DANIK 4
	RANK=1

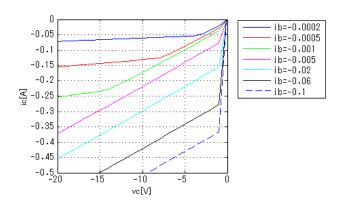
Functions	RANK	Implemented
IC-VBE(Temp)	1	0
IC-VCE-IB(Temp)	1	0
hFE-IC(Temp)	1	0
VCE(sat)-IC	1	0
VBE(sat)-IC	1	0
Capacitance	1	0
Transition	1	0
Switching	1	0



Simulation results are following. Explanatory notes — : simulated

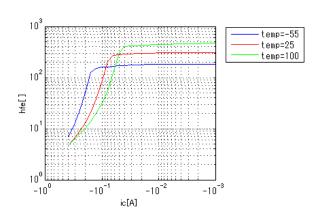
#### IcVce[ib]

Temp = 25degC



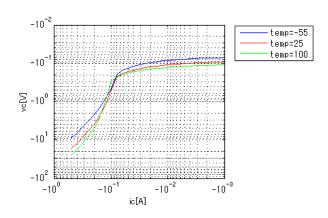
### hFElc[Temp]

Vce = -5V



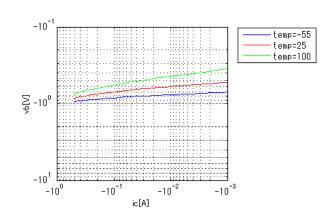
#### Vce(sat)lc[Temp]

IC/IB = 10



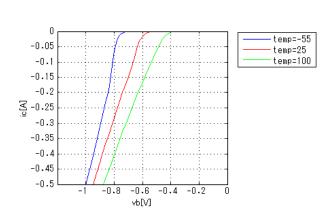
#### Vbe(sat)lc[Temp]

IC/IB = 10



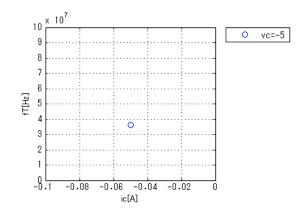
#### IcVbe[Temp]

Vce = -5V



#### fTIc[Vce]

Freq = 10000000Hz

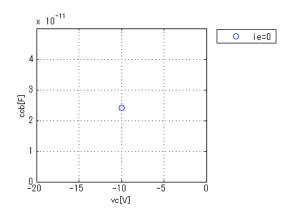




Simulation results are following. Explanatory notes — : simulated

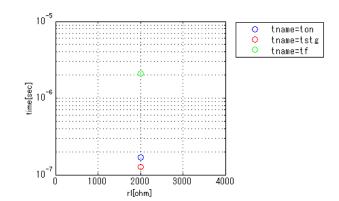
#### CobVcb

Freq = 1000000Hz



## SwitchingRI[Tname]

ib1 = -0.01, ib2 = 0.02, vcc = -200V, Temp = 25degC





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