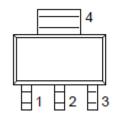
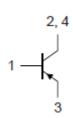


# PSpice Model PNP Nexperia BCP51





## **Model Information**

Model Gummel-Poon model
Call Name MDC\_BCP51\_PS
Pin Assign 1:B 2:C 3:E 4:C
File List Model Library

Model Library MDC\_BCP51\_PS01.lib

Model Report MDC\_BCP51\_PS.pdf (this file)

**Verified Simulator Version** 

Note

PSpice version 17.2

#### References

The information which was used for modeling is as follow:

[Data Sheet]

Date/Version Rev. 9 - 13 October 2011

Product nameBCP51Company nameNexperia B.V.

● Characteristics hFEIc[Temp],IcVce[ib],VbeIc[Temp],Vce(sat)Ic[Temp],CobVc

b,fTlc[Vce]

#### **Simulation Range**

This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

Item	Range			Unit
	Min.		Max.	
Collector-emitter voltage (DC)	0	to	-45	V
Collector current (DC)	0	to	-1	Α
Temperature	-65	to	150	deg C



**Model Functions Table** 

BJT

O: Implemented

× : Not Implemented

—: Not applicable

## RANK=1

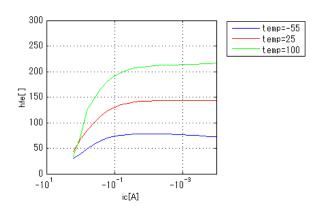
	10 11 11 2	
Functions	RANK	Implemented
IC-VBE(Temp)	1	0
IC-VCE-IB(Temp)	1	0
IC-hFE(Temp)	1	0
VCE(sat)-IC	1	0
VBE(sat)-IC	1	1
Capacitance	1	0
Transition	1	0
Switching	1	_



Simulation results are following. Explanatory notes — : simulated

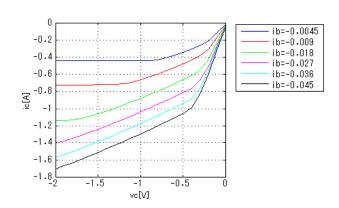
## hFElc[Temp]

Vce = -2V



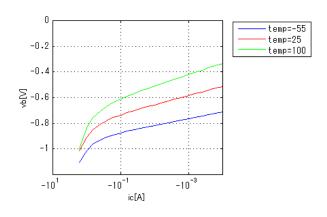
#### IcVce[ib]

Temp = 25degC



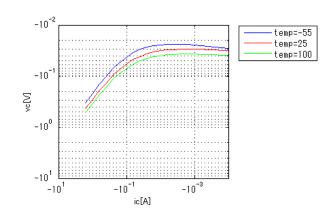
#### Vbelc[Temp]

Vce = -2V



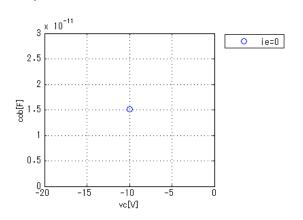
## Vce(sat)lc[Temp]

IC/IB = 10



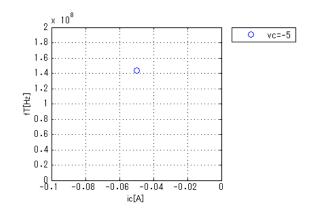
#### CobVcb

Freq = 1000000Hz



#### fTIc[Vce]

Freq = 100000000Hz





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