

MDC_TC7WZ125FK_PS

PSpice Model Logic Ic **TOSHIBA** TC7WZ125FK

Model Information

Model	A macro mode		
Call Name	MDC_TC7WZ125FK_PS		
Pin Assign	1:G1 2:A1 3:Y2 4:GND 5:A2 6:Y1 7:G2 8:VCC		
File List	Model Library	MDC_TC7WZ125FK_PS.lib	
	Model Report	MDC_TC7WZ125FK_PS.pdf(this file)	
Verified Simulator Version		PSpice 17.4	

Verified Simulator Version

Note

References

The information which was used for modeling is as follow:

[Data Sheet]

Date/Version	
Product name	
Company name	

Rev.4.0 TC7WZ125FK TOSHIBA

[Characteristics listed] Characteristics

Simulation Condition

This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

Item	Condition	Unit
Temperature	25	deg C



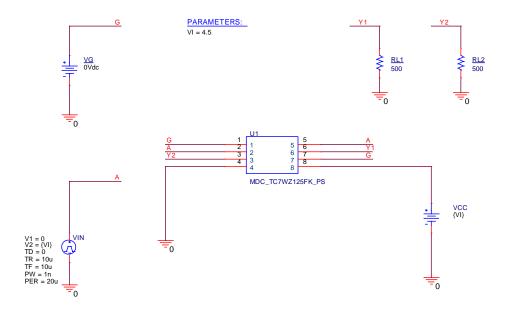
MDC_TC7WZ125FK_PS

Logic IC		O : Implemented × : Not Implemented	
Model Functions Table	RANK=1	— : Not applicable	
Functions	RANK	Implemented	
VIN-VOUT	1	0	
tpLH-HL(CL=15pF)	1	0	
tpLH-HL(CL=50pF)	1	0	
tpLZ-ZL	1	0	
tpHZ-ZH	1	0	



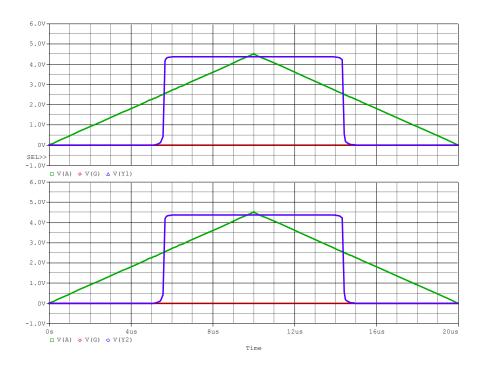
VIN-VOUT Testbench

Referred to Data Sheet



Simulation results are following. Explanatory notes — : simulated

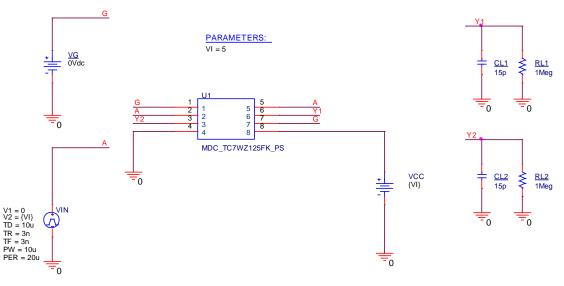
VIN-VOUT





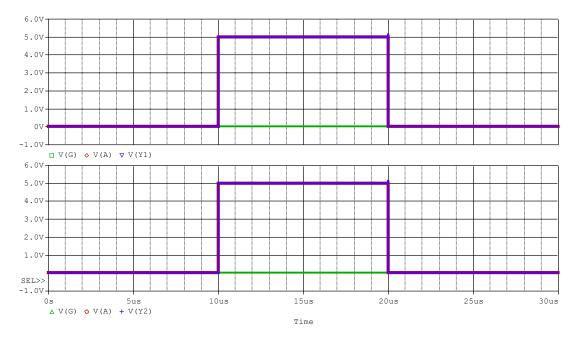
tpLH-HL(CL=15pF) Testbench

Referred to Data Sheet



Simulation results are following. Explanatory notes -: simulated

tpLH-HL(CL=15pF)

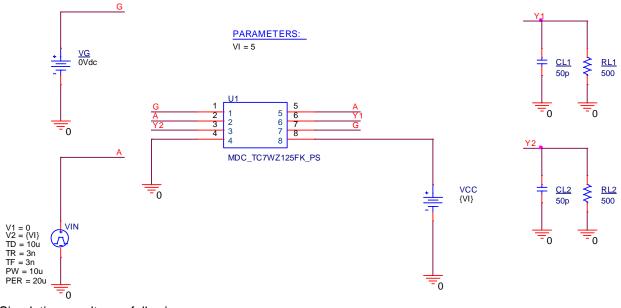


Measurement	Value
XatNth Y(V(Y1)23,1)- XatNth Y(V(A)23,1)	1.631.73n
XatNth Y(V(Y1),2.3,2)- XatNth Y(V(A),2.3,2)	987.34252p



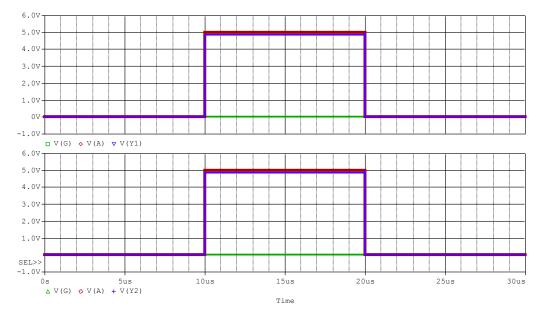
tpLH-HL(CL=50pF) Testbench

Referred to Data Sheet



Simulation results are following. Explanatory notes — : simulated

tpLH-HL(CL=50pF)

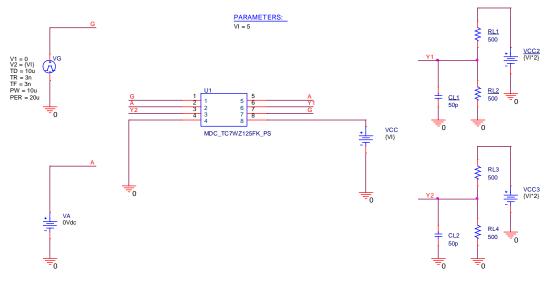


Measurement	Value
XatNth Y(V(Y1),2.3,1)- XatNth Y(V(A),2.3,1)	2.18600n
XatNth Y(V(Y1)23,2)- XatNth Y(V(A)23,2)	1.44621 n



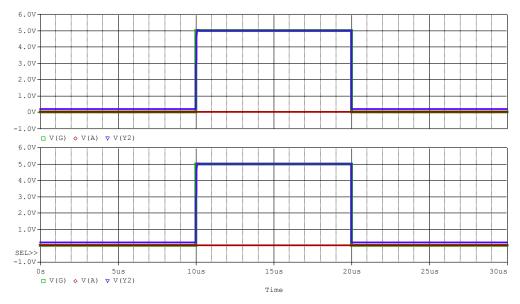
tpLZ-ZL Testbench

Referred to Data Sheet



Simulation results are following. Explanatory notes -: simulated

tpLZ-ZL



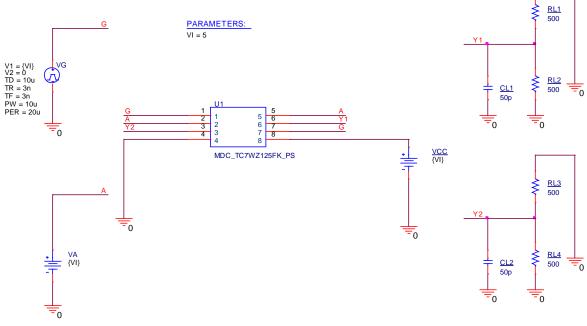
Measurement	Value
XatNth Y(V(Y1),2.3,2)-XatNth Y(V(G),2.3,2)	2.85085n
XatNth Y(V(M),0.51133,1)-XatNth Y(V(G),2.3,1)	3.97433n

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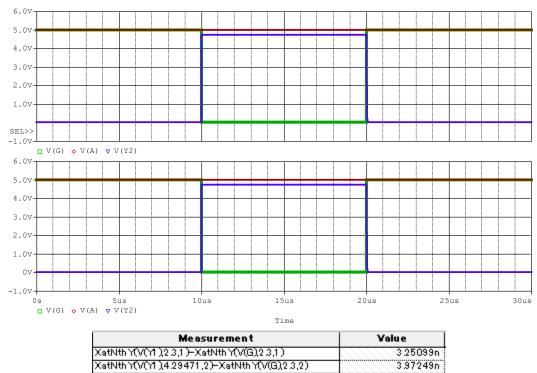
tpHZ-ZH Testbench





Simulation results are following. Explanatory notes — : simulated

tpHZ-ZH





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MoDeCH Inc.

Head Office Location: 5-15 Yokoyama-cho, Hachioji-Shi, Tokyo 192-0081, Japan Tel:+81-42-656-3360 E-Mail:model-on-support@modech.co.jp URL:http://www.modech.com/en/