

# LTspice Model

## Other IC

### TOREX

### XC6118N10BMR-G

#### Model Information

**Model** An original macro model  
**Call Name** MDC\_XC6118N10BMR-G\_LT  
**Pin Assign** 1:VOUT 2:VSS 3:VIN 4:VSEN 5:Cd  
**File List** Model Library MDC\_XC6118N10BMR-G\_LT01.lib  
 Model Report MDC\_XC6118N10BMR-G\_LT.pdf (this file)

**Verified Simulator Version** LTspice version XVII  
**Note**

#### References

The information which was used for modeling is as follow:

[Data Sheet]

- Date/Version ETR02013-005
- Product name XC6118N10BMR-G
- Company name TOREX SEMICONDUCTOR LTD.
- Characteristics Transient,Transient2

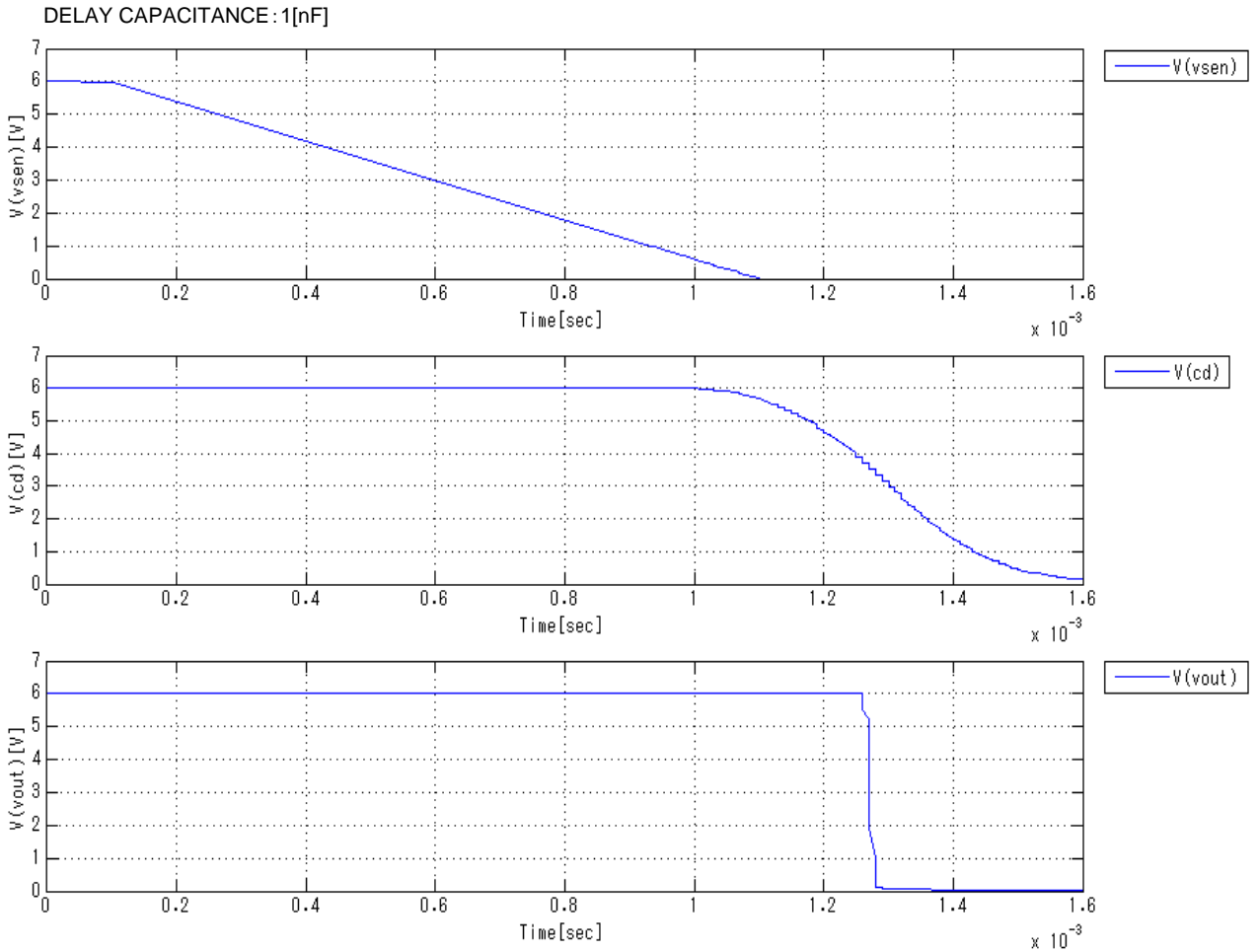
#### Simulation Condition

This table shows the value of evaluated simulation condition that was not occurs any convergence problems in this area.

Item	Condition	Unit
Temperature	25	deg C

Simulation results are following.  
Explanatory notes — : simulated

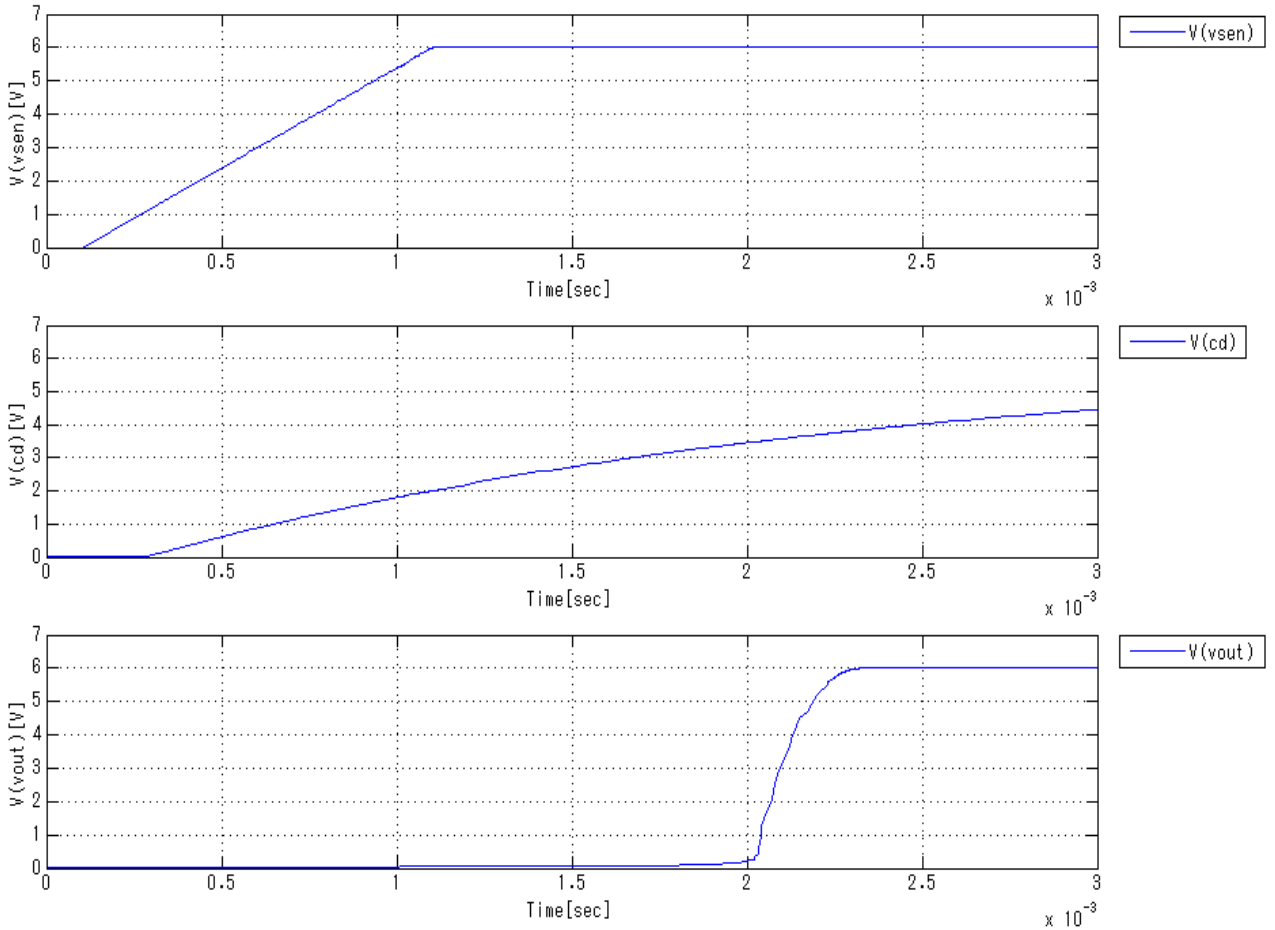
Transient



Simulation results are following.  
 Explanatory notes — : simulated

**Transient2**

DELAY CAPACITANCE : 1[nF]



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