

# LTspice Model Transistor Array TI ULN2803ADWR

## Model Information

**Model** An original macro model  
**Call Name** MDC\_ULN2803ADWR\_LT  
**Pin Assign** 1:1B 2:2B 3:3B 4:4B 5:5B 6:6B 7:7B 8:8B 9:GND 10:COM 11:8C 12:7C 13:6C 14:5C 15:4C 16:3C  
17:2C 18:1C  
**File List** Model Library MDC\_ULN2803ADWR\_LT01.lib  
Model Report MDC\_ULN2803ADWR\_LT.pdf (this file)  
**Verified Simulator Version** LTspice version XVII  
**Note**

## References

The information which was used for modeling is as follow:

[Data Sheet]

- Date/Version 2017-02
- Product name ULN2803ADWR
- Company name Texas Instruments Inc.
- Characteristics VCEICIB, Transient

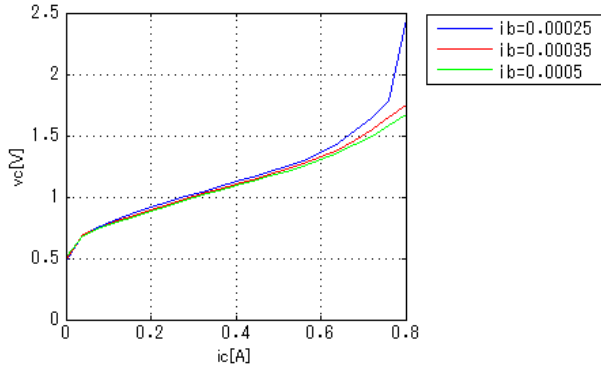
## Simulation Condition

This table shows the value of evaluated simulation condition that was not occurs any convergence problems in this area.

Item	Condition	Unit
Temperature	25	deg C

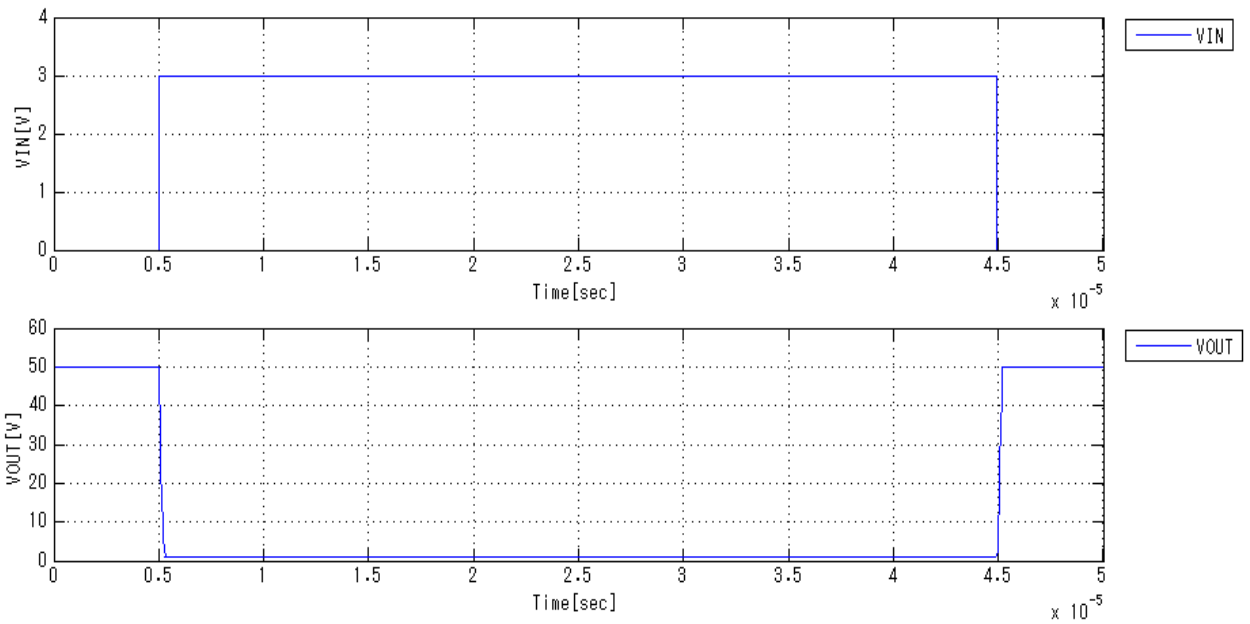
Simulation results are following.  
Explanatory notes — : simulated

**VCEICIB**



**Transient**

Temp. = 25deg C



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