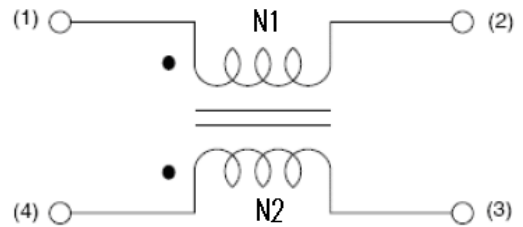


LTspice Model

Choke Coil

MURATA

DLW5ATH500MQ2



Model Information

Model An original macro model
Call Name MDC_DLW5ATH500MQ2_LT
Pin Assign 1:N1 2:N1 3:N2 4:N2
File List Model Library MDC_DLW5ATH500MQ2_LT01.lib
 Model Report MDC_DLW5ATH500MQ2_LT.pdf (this file)
Verified Simulator Version LTspice version XVII
Note

References

The information which was used for modeling is as follow:

[Data Sheet]

- Date/Version 2023/09/30
- Product name DLW5ATH500MQ2
- Company name Murata Manufacturing Co., Ltd.
- Characteristics ImpedanceFreq[NAME],ESRFreq[NAME],ReactanceFreq[NAME],ResistanceVf[Temp],InsulationResVr[Temp]

Simulation Range

This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

Item	Range			Unit
	Min.		Max.	
Temperature	-40	to	125	deg C

Choke Coil

○ : Implemented
 × : Not Implemented
 — : Not applicable

Model Functions Table

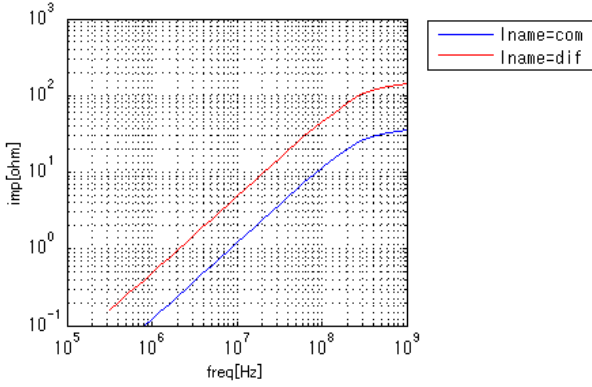
RANK=1

Functions	RANK	Implemented
Impedance-Frequency	1	○
ESR-Frequency	1	○
Reactance-Frequency	1	○
Resistance	1	○
Insulation Resistance	1	○

Simulation results are following.
 Explanatory notes — : simulated

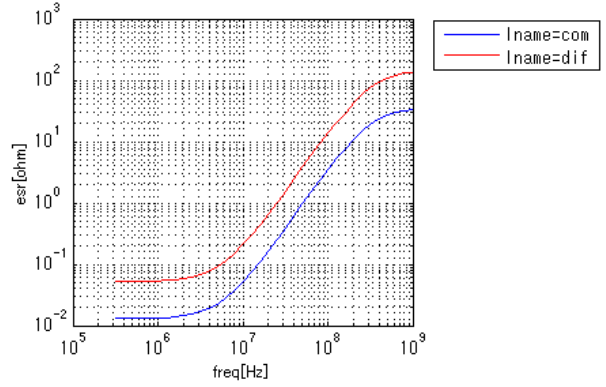
ImpedanceFreq[NAME]

temp = 25degC



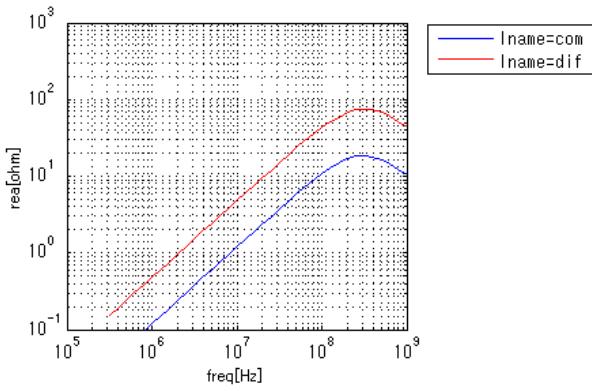
ESRFreq[NAME]

temp = 25degC

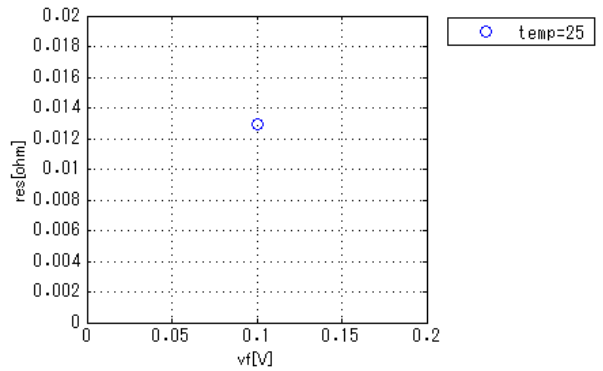


ReactanceFreq[NAME]

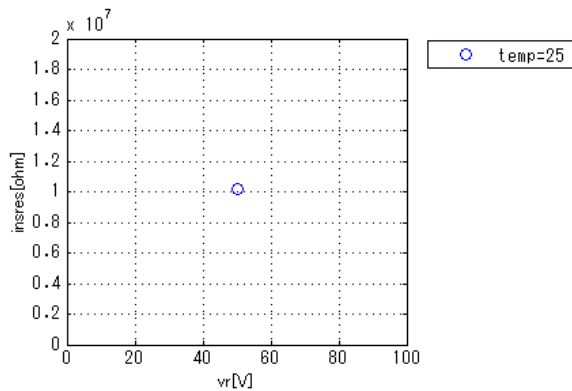
temp = 25degC



ResistanceVf[Temp]



InsulationResVr[Temp]



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