

### MDC\_TTA1452B\_PS

# PSpice Model PNP TOSHIBA TTA1452B

## **Model Information**

ModelMextram modelCall NameMDC\_TTA1452B\_PSPin Assign1:C 2:B 3:EFile ListModel Library<br/>Model Report

MDC\_TTA1452B\_PS01.lib MDC\_TTA1452B\_PS.pdf (this file)

Verified Simulator Version Note

PSpice version 16.6

#### References

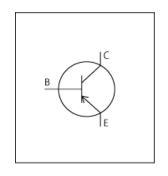
The information which was used for modeling is as follow:

2015-08-06 Rev 4.0 TTA1452B Toshiba Corporation IcVce[ib],hFEIc[Temp],Vce(sat)Ic[Temp],Vbe(sat)Ic[Temp],Ic Vbe[Temp]

#### Simulation Range

This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

| Item                           | Range |    |      | Unit  |
|--------------------------------|-------|----|------|-------|
|                                | Min.  |    | Max. |       |
| Collector-emitter voltage (DC) | 0     | to | -80  | V     |
| Base current (DC)              | 0     | to | -2   | А     |
| Temperature                    | -55   | to | 150  | deg C |

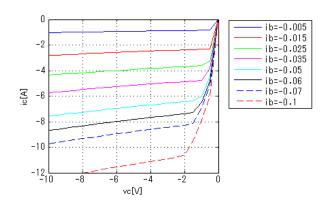




#### Simulation results are following. Explanatory notes — : simulated

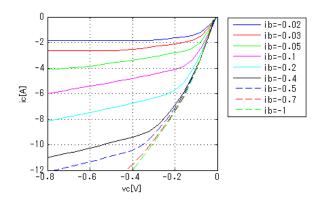
#### IcVce[ib]

Temp. = 25deg C



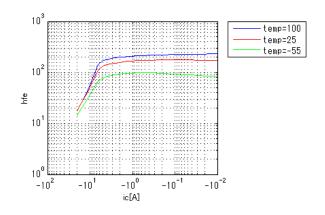
#### IcVce[ib]

Temp. = -55deg C



hFElc[Temp]

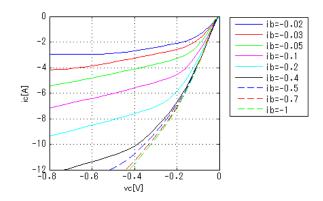
Vce = -1V



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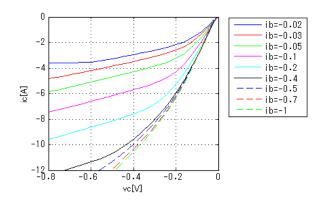
#### lcVce[ib]

Temp. = 25deg C



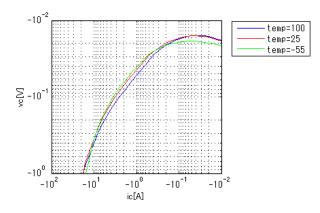
#### lcVce[ib]

Temp. = 100deg C



## Vce(sat)lc[Temp]

IC/IB = 20

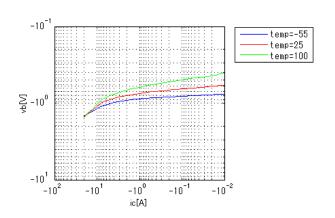




# Simulation results are following. Explanatory notes -: simulated

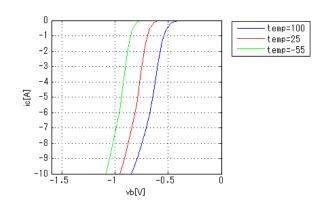
### Vbe(sat)Ic[Temp]

IC/IB = 20



### IcVbe[Temp]

Vce = -1V





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