

# PSpice Model

## PNP

## ON

## SNSS35200MR6T1G



### Model Information

**Model** Gummel-Poon model  
**Call Name** MDC\_SNSS35200MR6T1G\_PS  
**Pin Assign** 1:C 2:B 3:E  
**File List** Model Library MDC\_SNSS35200MR6T1G\_PS02.lib  
 Model Report MDC\_SNSS35200MR6T1G\_PS.pdf (this file)

**Verified Simulator Version** PSpice version 16.6  
**Note**

### References

The information which was used for modeling is as follow:

[Data Sheet]

- Date/Version Rev. 5
- Product name SNSS35200MR6T1G
- Company name ON Semiconductor.
- Characteristics  $V_{ce(sat)}(I_c[Temp], h_{FE}(I_c[Temp], V_{be(sat)}(I_c[Temp], I_c V_{be}[Temp]), C_{ob}, C_{ib}$

### Simulation Range

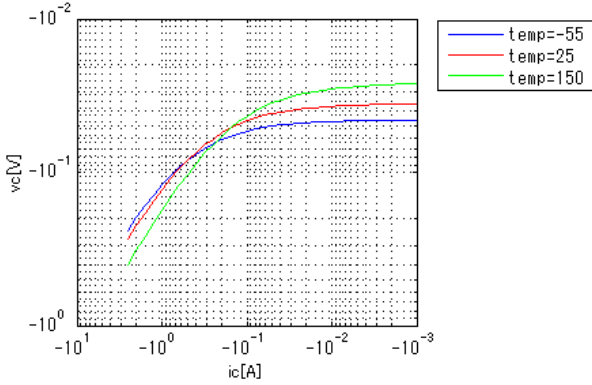
This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

Item	Range			Unit
	Min.		Max.	
Collector-emitter voltage (DC)	0	to	-35	V
Base current (DC)	0	to	-0.040	A
Temperature	-55	to	150	deg C

Simulation results are following.  
 Explanatory notes — : simulated

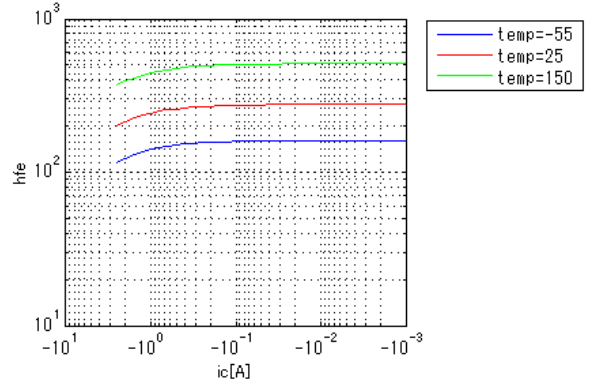
**Vce(sat)Ic[Temp]**

IC/IB = 100



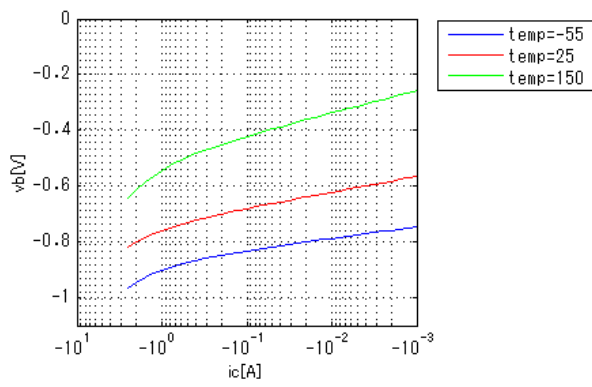
**hFEIc[Temp]**

Vce = -1.5V



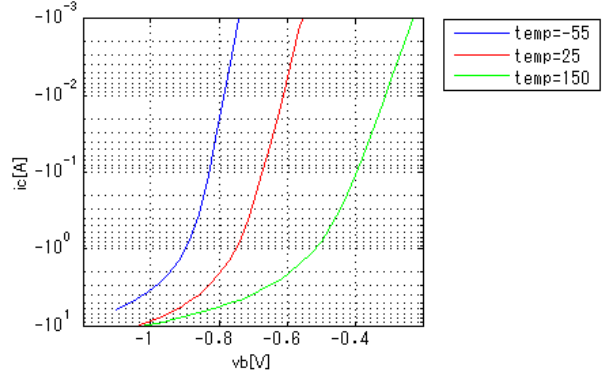
**Vbe(sat)Ic[Temp]**

IC/IB = 100



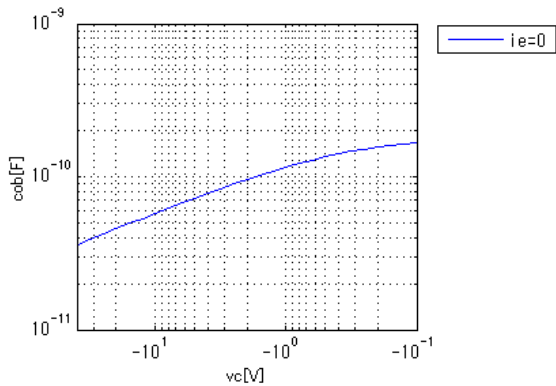
**IcVbe[Temp]**

Vce = -3V



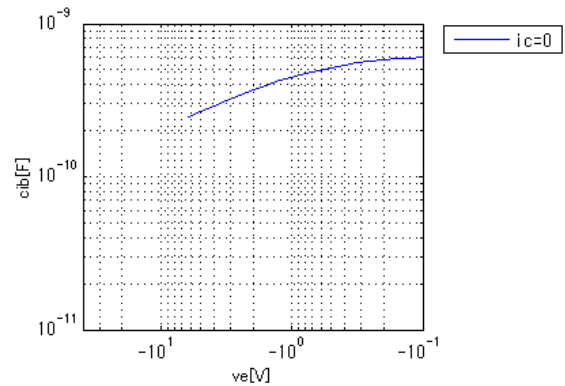
**Cob**

Freq. = 1MHz



**Cib**

Freq. = 1MHz



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