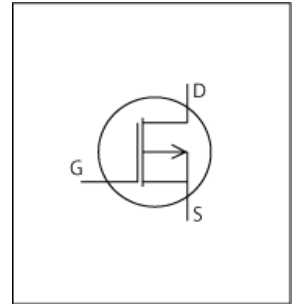


PSpice Model

PMOS

Isahaya

INJ0203BC1



Model Information

Model A macro model based on BSIM3
Call Name MDC_INJ0203BC1_PS
Pin Assign 1:D 2:G 3:S
File List Model Library MDC_INJ0203BC1_PS01.lib
 Model Report MDC_INJ0203BC1_PS.pdf (this file)

Verified Simulator Version PSpice version 16.6
Note

References

The information which was used for modeling is as follow:

[Data Sheet]

- Date/Version 2016/9
- Product name INJ0203BC1
- Company name Isahaya Electronics Corporation
- Characteristics IdVgs[Temp], IsVsd[Temp], Rds(on)Id[Temp], Rds(on)Vgs[Temp], VthTemp[Id], Coss, Ciss

Simulation Range

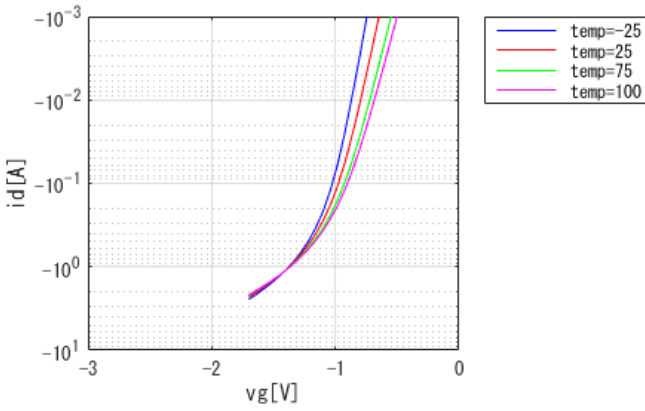
This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

Item	Range			Unit
	Min.		Max.	
Drain-source voltage (DC)	0	to	-20	V
Gate-source voltage (DC)	0	to	-10	V
Temperature	-55	to	150	deg C

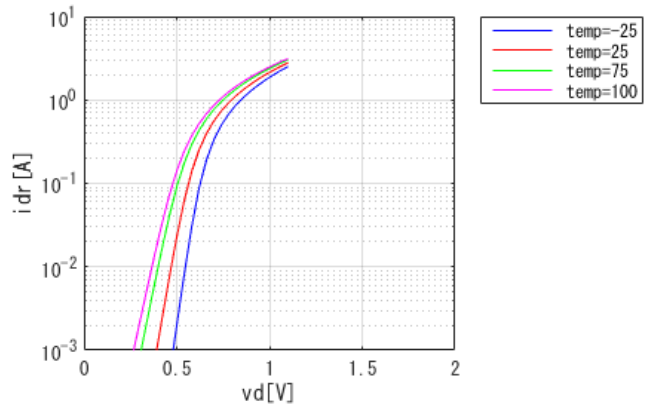
Simulation results are following.
 Explanatory notes — : simulated

IdVgs[Temp]

Vds = -10V

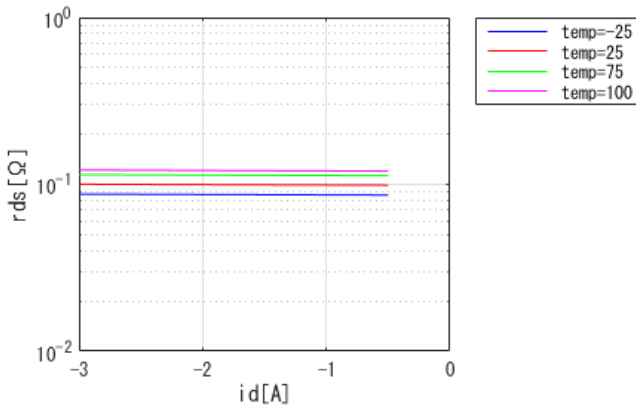


IsVsd[Temp]



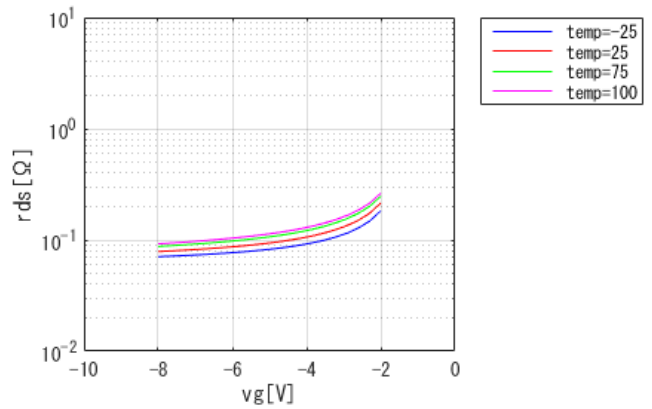
Rds(on)Id[Temp]

Vgs = -4.5V

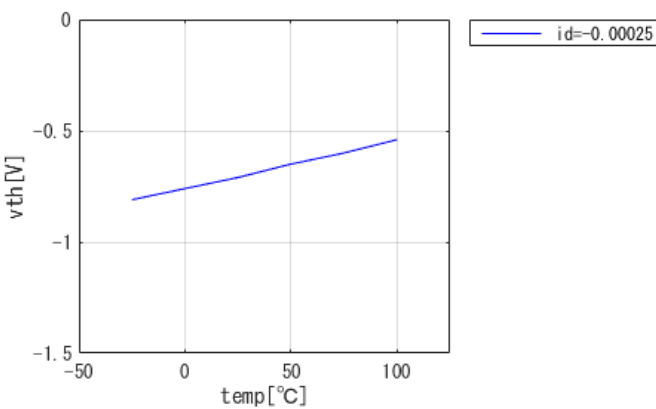


Rds(on)Vgs[Temp]

Id = -1A

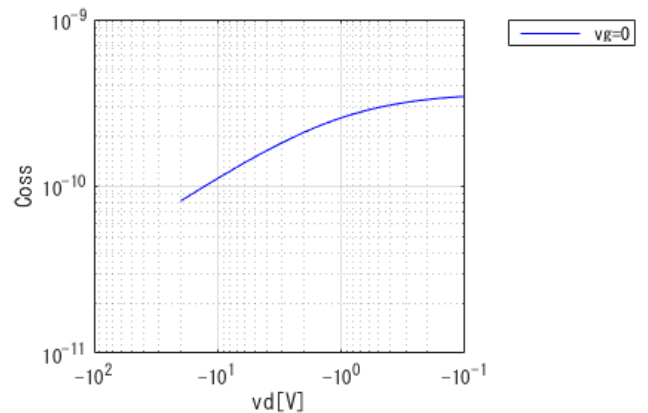


VthTemp[Id]



Coss

Freq. = 1MHz

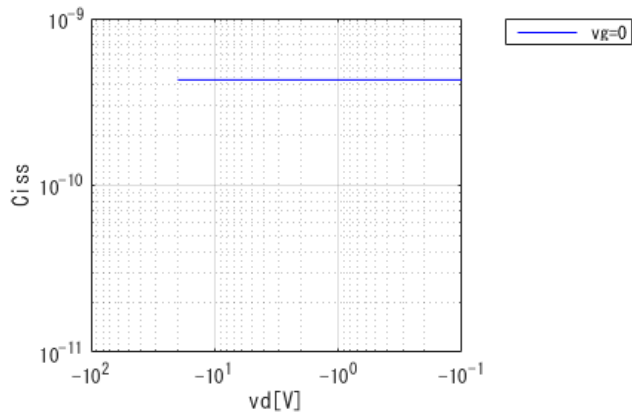


Simulation results are following.

Explanatory notes — : simulated

Ciss

Freq. = 1MHz



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