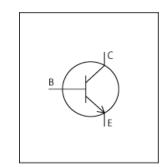


PSpice Model NPN ON FJBE2150D



Model Information

Model Gummel-Poon model Call Name MDC_FJBE2150D_PS

Pin Assign 1:C 2:B 3:E File List Model Librar

Model Library MDC_FJBE2150D_PS02.lib
Model Report MDC_FJBE2150D_PS.pdf (this file)

Verified Simulator Version

Note

PSpice version 16.6

References

The information which was used for modeling is as follow:

[Data Sheet]

Date/Version Rev. 1.4Product name FJBE2150D

Company name ON Semiconductor.

● Characteristics IcVce[ib],hFEIc[Temp],Vce(sat)Ic[Temp],Vce(sat)Ib[Ic],Cob

Simulation Range

This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

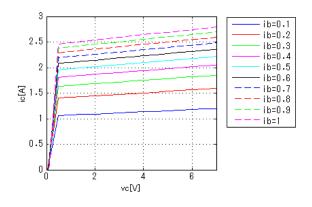
Item	Range			Unit
	Min.		Max.	
Collector-emitter voltage (DC)	0	to	800	V
Base current (DC)	0	to	1	Α
Temperature	-65	to	150	deg C



Simulation results are following. Explanatory notes — : simulated

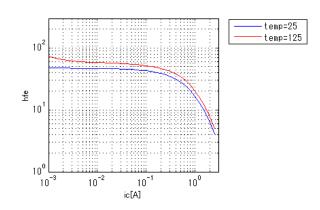
IcVce[ib]

Temp. = 25deg C



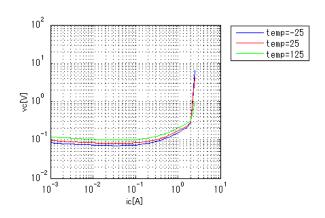
hFEIc[Temp]

Vce = 10V

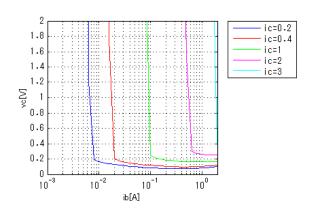


Vce(sat)lc[Temp]

IC/IB = 3

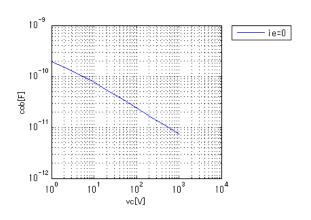


Vce(sat)lb[lc]



Cob

Freq. = 1MHz





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