

LTspice Model

LDO (Low Drop Out)

TOREX

XC6219F332PR-G

Model Information

Model An original macro model
Call Name MDC_XC6219F332PR-G_LT
Pin Assign 1:NC 2:VSS 3:CE 4:VIN 5:VOUT
File List Model Library MDC_XC6219F332PR-G_LT01.lib
 Model Report MDC_XC6219F332PR-G_LT.pdf (this file)

Verified Simulator Version LTspice version XVII
Note

References

The information which was used for modeling is as follow:

[Data Sheet]

- Date/Version ETR0307_010a
- Product name XC6219F332PR-G
- Company name TOREX SEMICONDUCTOR LTD.
- Characteristics Vdiflout[Temp],Line regulation,Load regulation

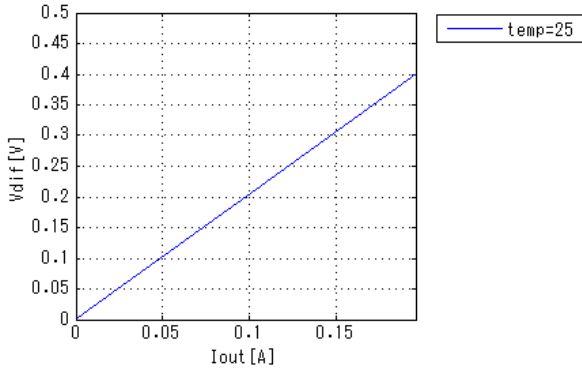
Simulation Condition

This table shows the value of evaluated simulation condition that was not occurs any convergence problems in this area.

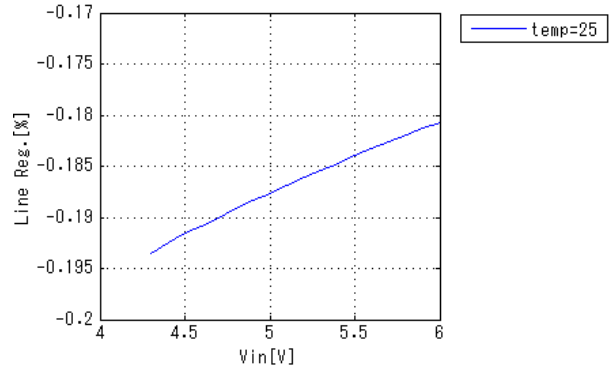
Item	Condition	Unit
Temperature	25	deg C

Simulation results are following.
 Explanatory notes — : simulated

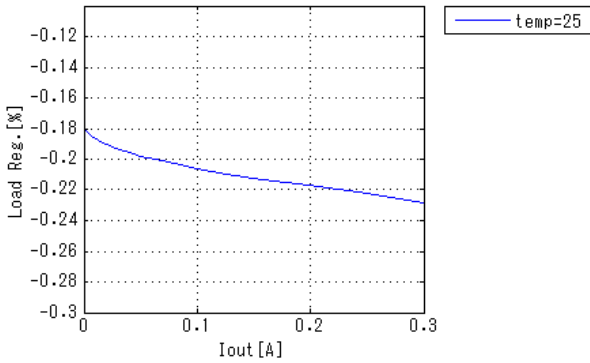
Vdiflout[Temp]



Line regulation



Load regulation



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