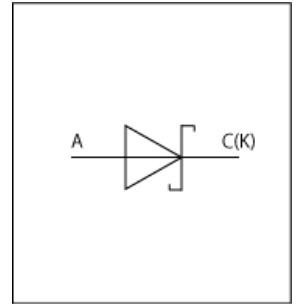


PSpice Model Schottky Diode VISHAY VSSAF56HM3



Model Information

Model A macro model based on general SPICE diode model
Call Name MDC_VSSAF56HM3_PS
Pin Assign 1:A 2:C
File List Model Library MDC_VSSAF56HM3_PS01.lib
 Model Report MDC_VSSAF56HM3_PS.pdf (this file)

Verified Simulator Version PSpice version 16.6
Note

References

The information which was used for modeling is as follow:

[Data Sheet]

- Date/Version 05-May-15
- Product name VSSAF56HM3
- Company name Vishay Intertechnology, Inc.
- Characteristics IfVf[Temp],Irvr[Temp],CjVr

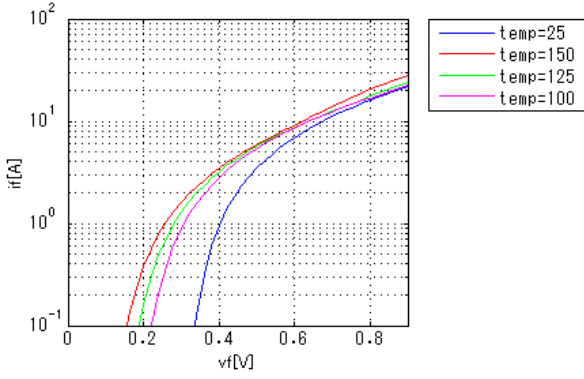
Simulation Range

This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

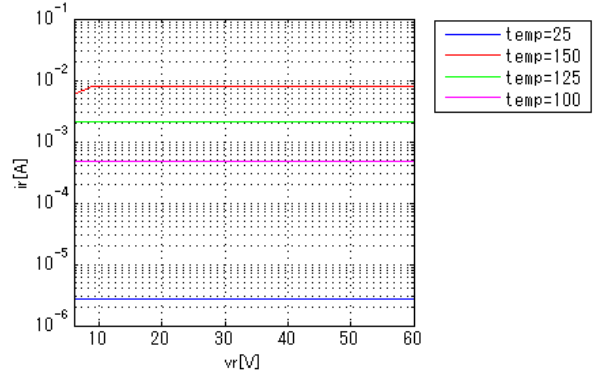
Item	Range			Unit
	Min.		Max.	
Forward Voltage	0	to	9	V
Reverse Voltage	0	to	120	V
Temperature	-40	to	150	deg C

Simulation results are following.
 Explanatory notes — : simulated

IfVr[Temp]

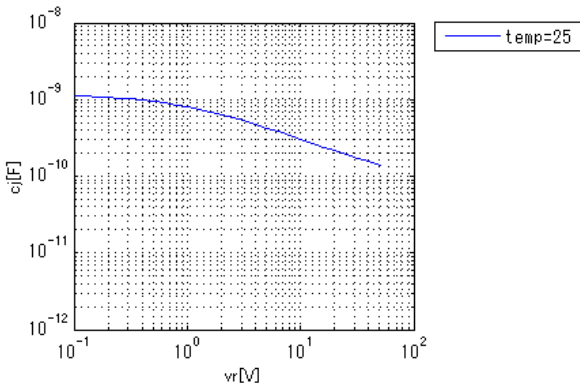


IrVr[Temp]



CjVr

Freq. = 1MHz



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