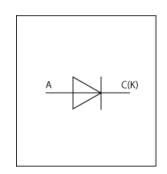


PSpice Model ESD Protection Diode (TVS) Microsemi MSMLJ10AE3



Model Information

Model A macro model based on general SPICE diode model

Call Name MDC_MSMLJ10AE3_PS

Pin Assign 1:A 2:C

File List Model Library MDC_MSMLJ10AE3_PS01.lib

Model Report MDC_MSMLJ10AE3_PS.pdf (this file)

Verified Simulator Version

Note

PSpice version 17.2

References

The information which was used for modeling is as follow:

[Data Sheet]

Date/VersionProduct nameMSMLJ10AE3

Company name Microsemi Corporation

● Characteristics IrVr[Temp], BreakdownVoltage, ClampingVoltage, LekageCurr

ent,CjVr,SurgeCurrentWaveform

Simulation Range

This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

Item	Range			Unit
	Min.		Max.	
Reverse Breakdown Voltage (typ)	0	to	11.7(at 1mA)	V
Reverse Clamping Voltage (max)	0	to	17(at 176.4A)	V
Temperature	-65	to	175	deg C



Model Functions Table

Diode

O: Implemented

×: Not Implemented

—: Not applicable

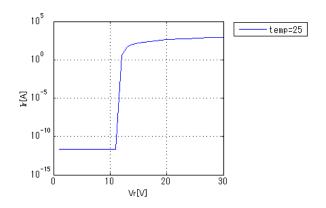
RANK=1

	IV WINT-1	
Functions	RANK	Implemented
IF-VF(Temp)	1	_
IR-VR(Temp)	1	0
Capacitance	1	0
Reverse recovery characteristics	1	_
Zz-Iz	1	_
Rectification characteristics(Bridge)	1	_
Surge-Transient	1	0
tlp	1	_



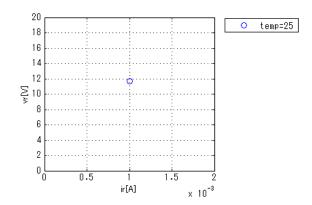
Simulation results are following. Explanatory notes — : simulated

IrVr[Temp]



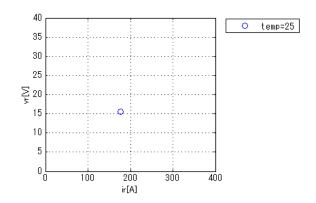
VBR(Breakdown Voltage)

at IR=0.001A



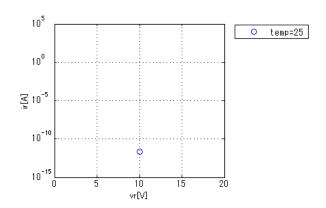
VC(Clamping Voltage)

at IPPM=176.4A max-10%



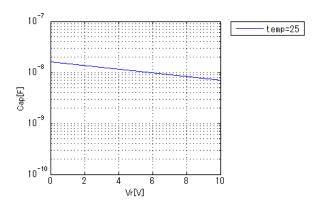
Leakage Current

at VWM(StandOff)=10V max(5uA)/1000 under



CjVr

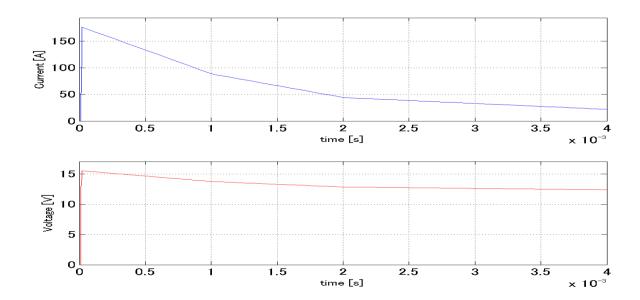
Freq = 1000000Hz





Simulation results are following. Explanatory notes — : simulated

Surge Current Waveform (Reverse 10u/1000u)





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MoDeCH Inc.

Head Office

Location: 5-15 Yokoyama-cho, Hachioji-Shi, Tokyo 192-0081, Japan

Tel:+81-42-656-3360

E-Mail:model-on-support@modech.co.jp

URL:http://www.modech.com/en/