

# PSpice Model

## Inductor

### Panasonic

### ETQP8MR68JFA

#### Model Information

**Model** A macro model  
**Call Name** MDC\_ETQP8MR68JFA\_PS  
**Pin Assign** 1:P1 2:P2  
**File List** Model Library MDC\_ETQP8MR68JFA\_PS01.lib  
 Model Report MDC\_ETQP8MR68JFA\_PS.pdf(this file)  
**Verified Simulator Version** Pspice 17.2

#### Note

#### References

The information which was used for modeling is as follow:

- [Data Sheet]
- Date/Version 2020/2/1
  - Product name ETQP8MR68JFA
  - Company name Panasonic

- [Characteristics listed]
- Characteristics LvsIDC

#### Simulation Condition

This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

Item	Condition	Unit
Temperature	25	deg C

**Model Functions Table**

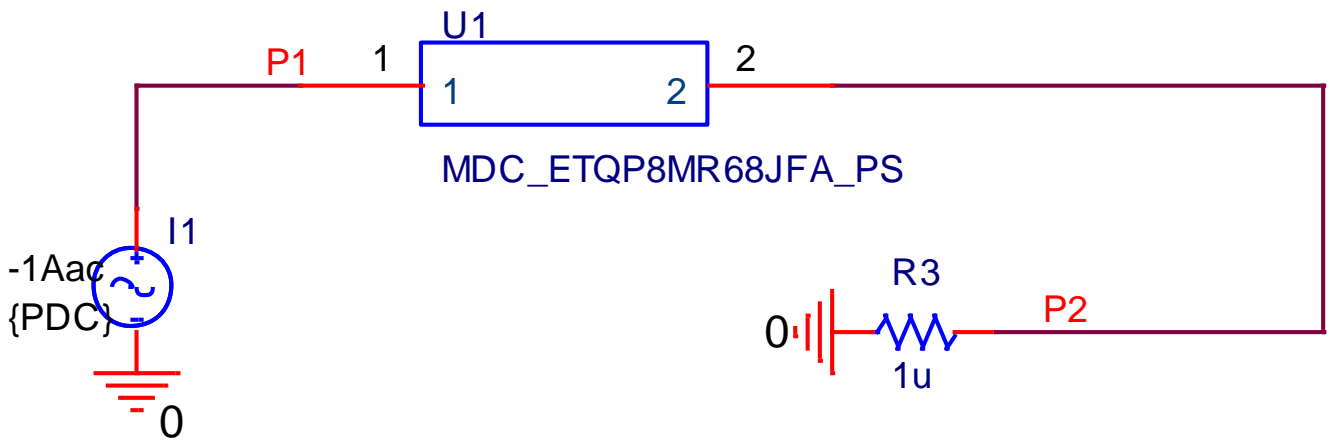
Functions	Implemented
L vs IDC	○

LvsIDC Testbench

Referred to Data Sheet

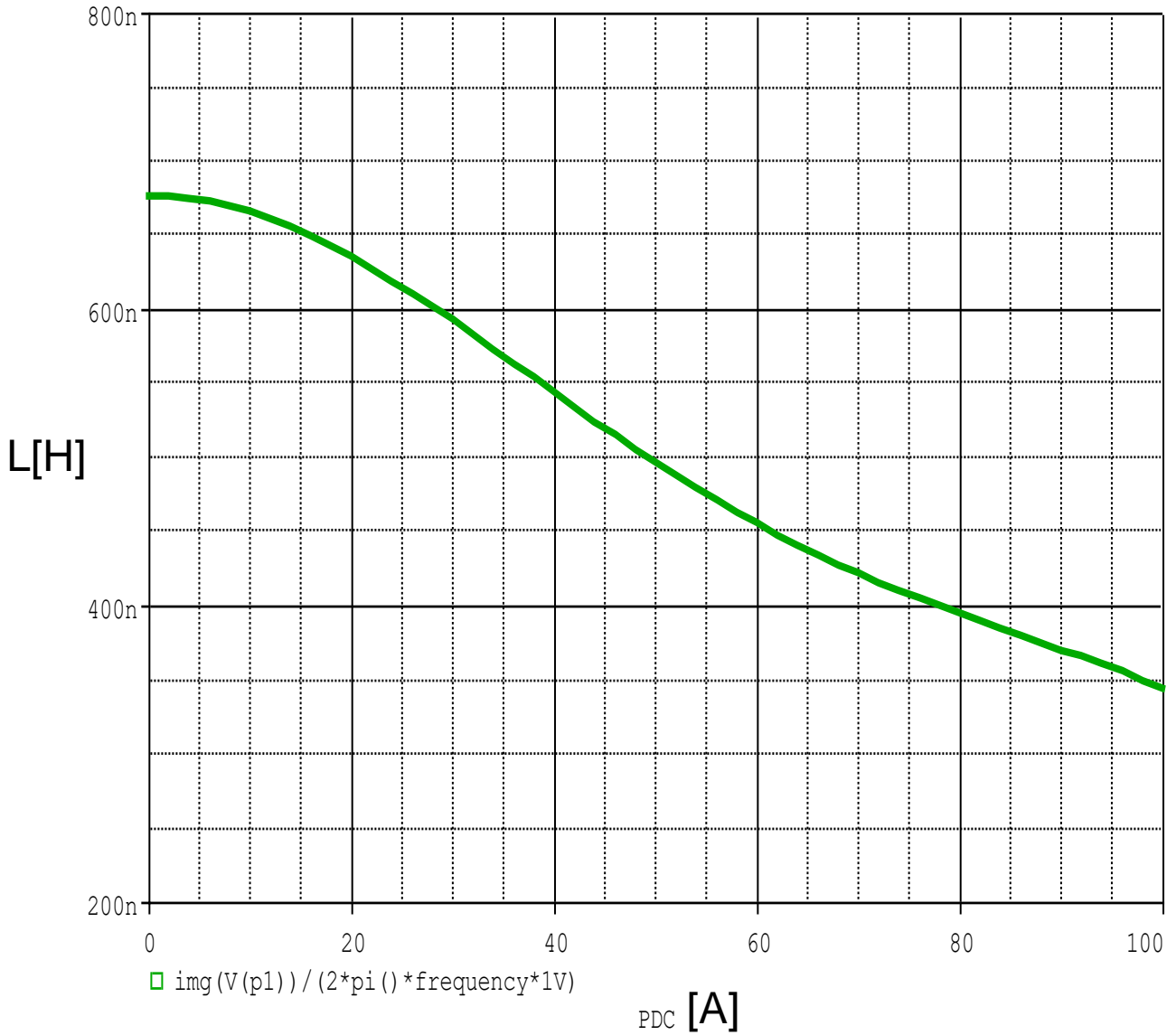
PARAMETERS:

PDC = 0



Simulation results are following.  
 Explanatory notes — : simulated

**LvsIDC**



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