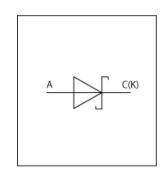


# PSpice Model Schottky Diode VISHAY V60D120CHM3



## **Model Information**

Model A macro model based on general SPICE diode model

Call Name MDC\_V60D120CHM3\_PS

Pin Assign 1:A 2:C

File List Model Library MDC\_V60D120CHM3\_PS01.lib

Model Report MDC\_V60D120CHM3\_PS.pdf (this file)

**Verified Simulator Version** 

Note

PSpice version 16.6

#### References

The information which was used for modeling is as follow:

[Data Sheet]

Date/VersionProduct name06-Feb-15V60D120CHM3

Company nameCharacteristicsVishay Intertechnology, Inc.IfVf[Temp],IrVr[Temp],CjVr

#### Simulation Range

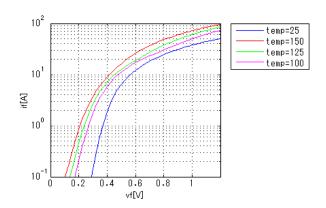
This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

Item	Range			Unit
	Min.		Max.	
Forward Voltage	0	to	12	V
Reverse Voltage	0	to	240	V
Temperature	-40	to	150	deg C

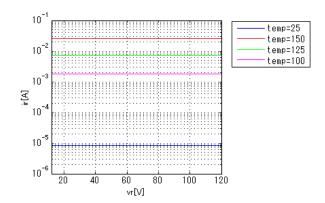


Simulation results are following. Explanatory notes — : simulated

# IfVf[Temp]

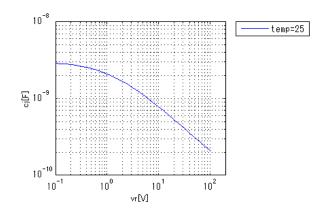


# IrVr[Temp]



#### CjVr

Freq. = 1MHz





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