

# PSpice Model Voltage Detector ABLIC Inc. S-19110FAMH

## **Model Information**

Model A macro model

Call Name MDC S-19110FAMH LT

Pin Assign 1:VDD 2:SENSE 3:OUT 4:CP 5:VSS 6:CN File List Model Library MDC\_S-19110FAMH\_LT01.lib

Model Report MDC\_S-19110FAMH\_LT.pdf(this file)

Verified Simulator Version LTspice XVII

Note

#### References

The information which was used for modeling is as follow:

[Data Sheet]

Date/VersionProduct nameCompany nameRev. 1.4\_00S-19110FAMHABLIC Inc.

[Characteristics listed]

Characteristics -VDET, +VDETtDELAY, tRESETRSENSE

#### **Simulation Condition**

This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

Item	Condition	Unit
Temperature	25	deg C



#### **Model Functions Table**

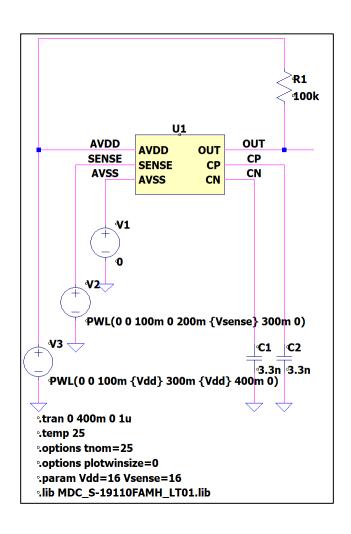
Functions	Implemented
SENSE detection	0
Delay Circuit	0

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#### Test bench for voltage detection

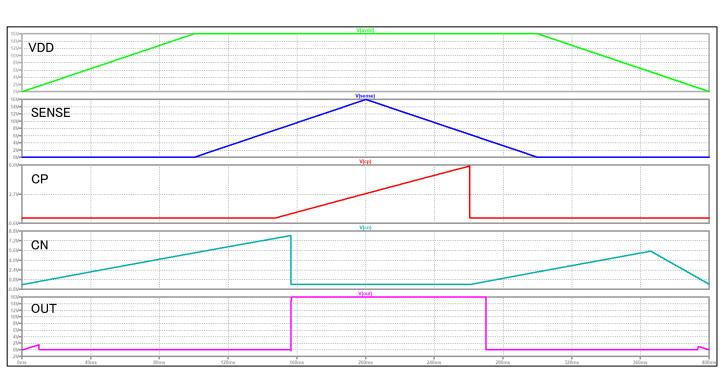
# **Referred to Data Sheet**





Simulation results are following. Explanatory notes - : simulated

# **Voltage Detect**





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