

LTspice Model Fast Recovery Diode LITE-ON UD7G

Model Information

 Model
 A macro model based on general SPICE diode model

 Call Name
 MDC_UD7G_LT

 Pin Assign
 1:A 2:C

 File List
 Model Library

 Model Report
 MDC_UD7G_LT.pdf (this file)

Verified Simulator Version Note

LTspice version XVII

References

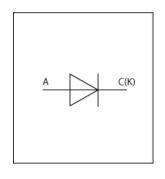
The information which was used for modeling is as follow:

REV.-0, Oct-2019 UD7G LITE-ON JAPAN LTD. CjVr,IfVf[Temp],IrVr[Temp],IrVr[Temp]2,TrrIfIr,TrrWaveform, SurgeWaveform

Simulation Range

This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

Item	Range			Unit
	Min.		Max.	
Reverse Voltage	0	to	1,000	V
Temperature	-55	to	150	deg C

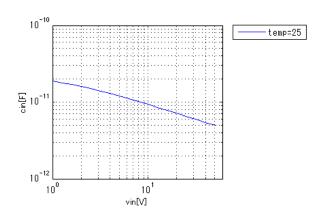




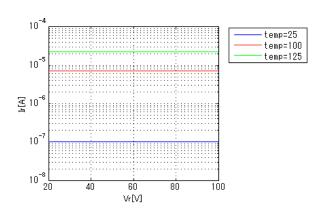
Simulation results are following. Explanatory notes — : simulated



Freq. = 1MHz

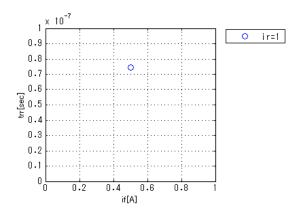


IrVr[Temp]



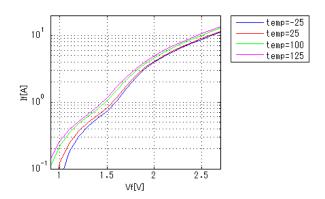
Trrlflr

Irr. = 0.25A, didt. = 25A/us

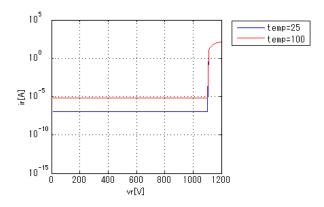


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lfVf[Temp]



lrVr[Temp]2

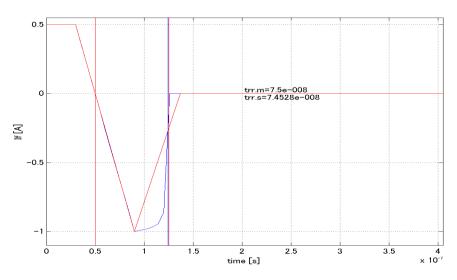




Simulation results are following. Explanatory notes — : simulated

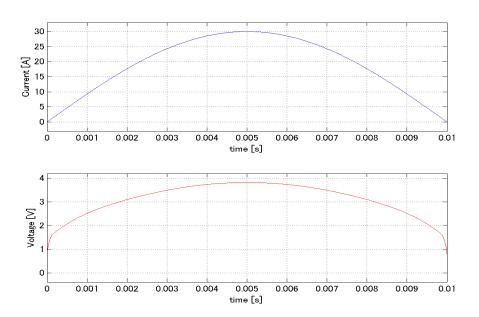
Trr Waveform red : datasheet (Trr=75ns) blue : simulation

If = 0.5A Ir = 1A Irr. = 0.25A, didt. = 25A/us



Surge Waveform

50Hz Sine Harf IFSM = 30A





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MoDeCH Inc.

Head Office Location: Taiju-Seimei-Hachioji Bldg., 5-15 Yokoyama-cho, Hachioji-Shi, Tokyo 192-0081, Japan Tel:+81-42-656-3360 E-Mail:model-on-support@modech.co.jp URL:http://www.modech.com/en/