

PSpice Model

LDO (Low Drop Out)

Infineon

TLS850F1TAV50

Model Information

Model An original macro model
Call Name MDC_TLS850F1TAV50_PS
Pin Assign 1:I 2:EN 3:RO/WO 4:GND 5:RADJ 6:WI 7:Q
File List Model Library MDC_TLS850F1TAV50_PS01.lib
Model Report MDC_TLS850F1TAV50_PS.pdf (this file)

Verified Simulator Version PSpice version 17.2
Note

References

The information which was used for modeling is as follow:

[Data Sheet]

- Date/Version Rev. 1.0
- Product name TLS850F1TAV50
- Company name Infineon Technologies AG
- Characteristics Vdrop,Line-Tran,Load-Tran,EN,RESET,WD

Simulation Condition

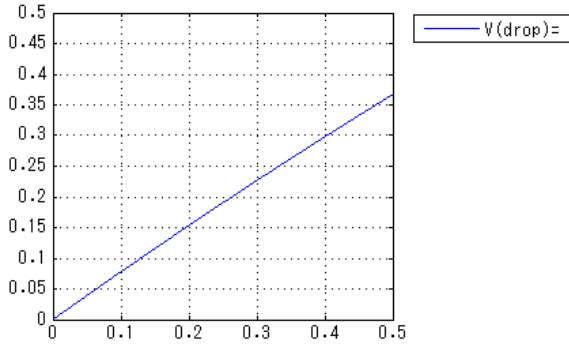
This table shows the value of evaluated simulation condition that was not occurs any convergence problems in this area.

Item	Condition	Unit
Temperature	25	deg C

Simulation results are following.
Explanatory notes — : simulated

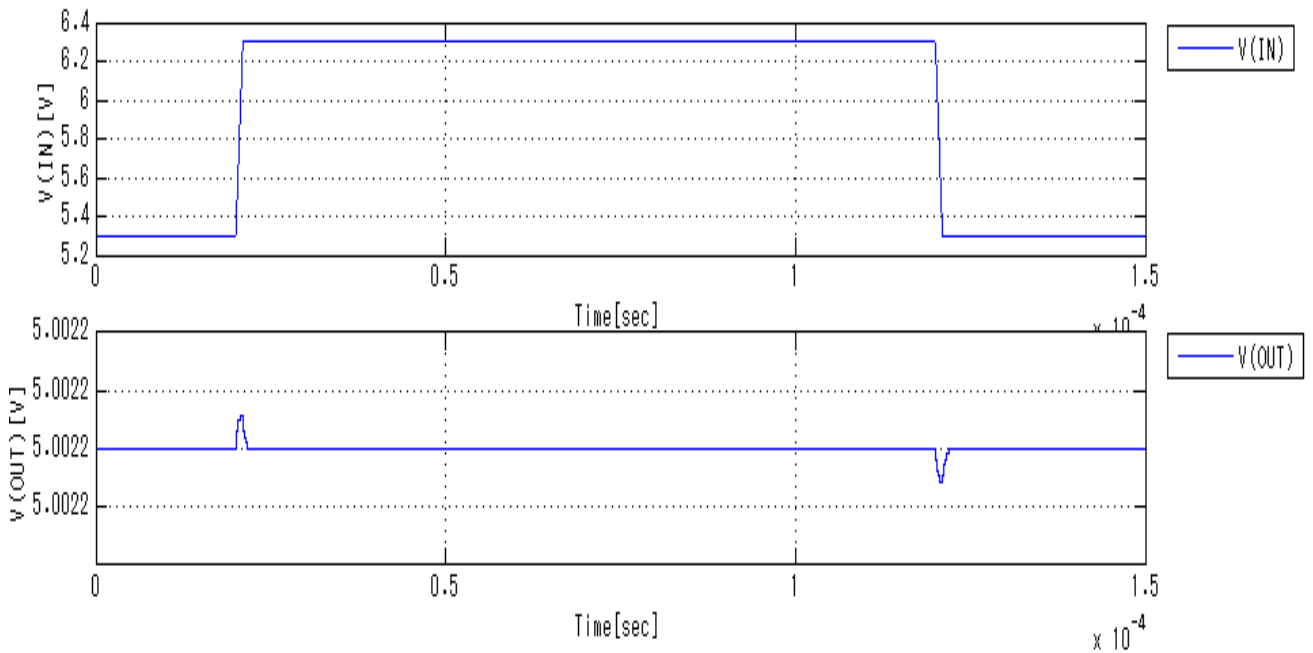
Vdrop

Temp. = 25deg C



Line-Tran

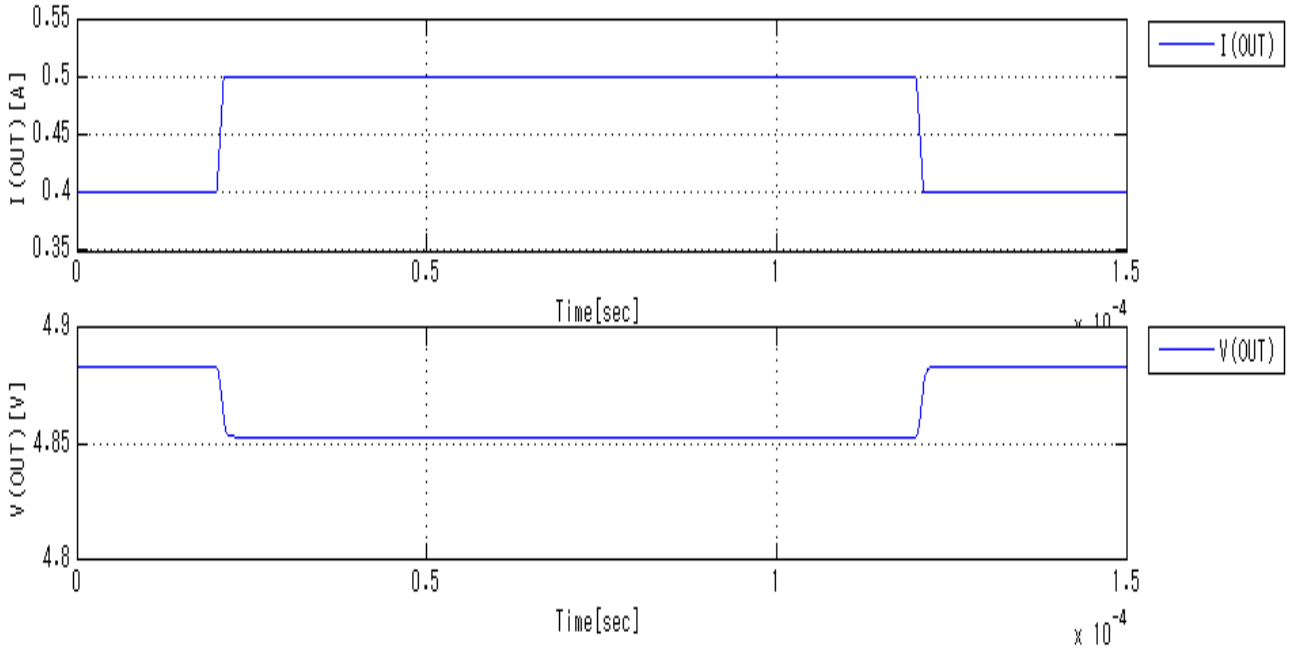
Temp. = 25deg C



Simulation results are following.
 Explanatory notes — : simulated

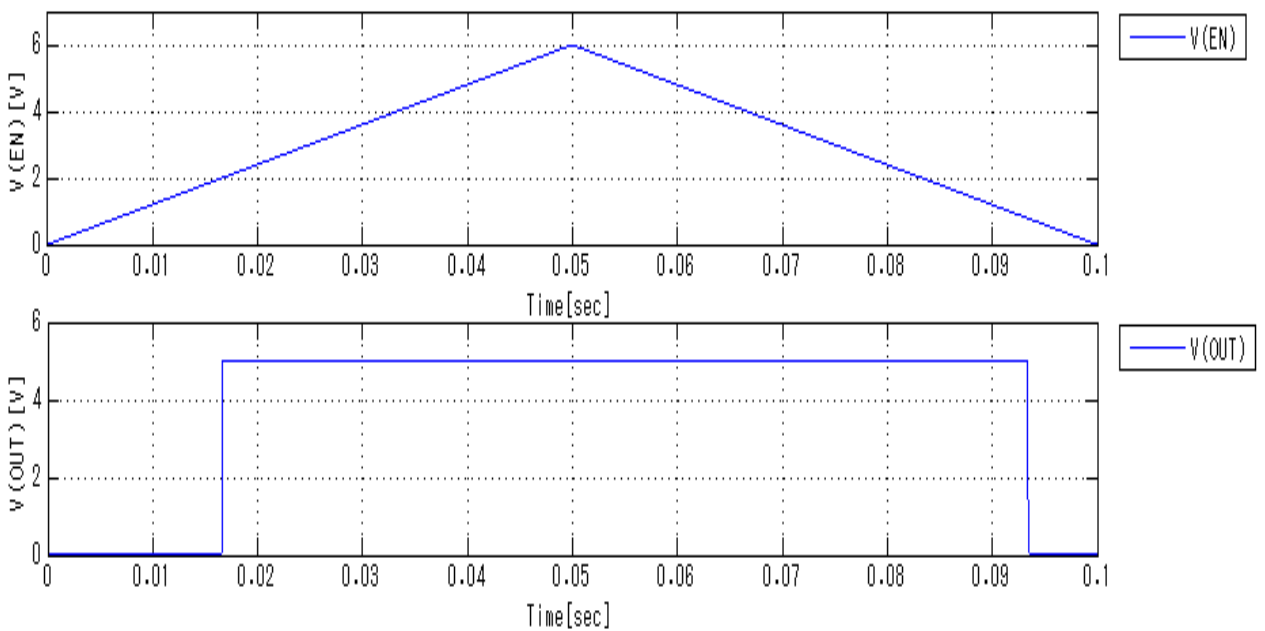
Load-Tran

Temp. = 25deg C



EN

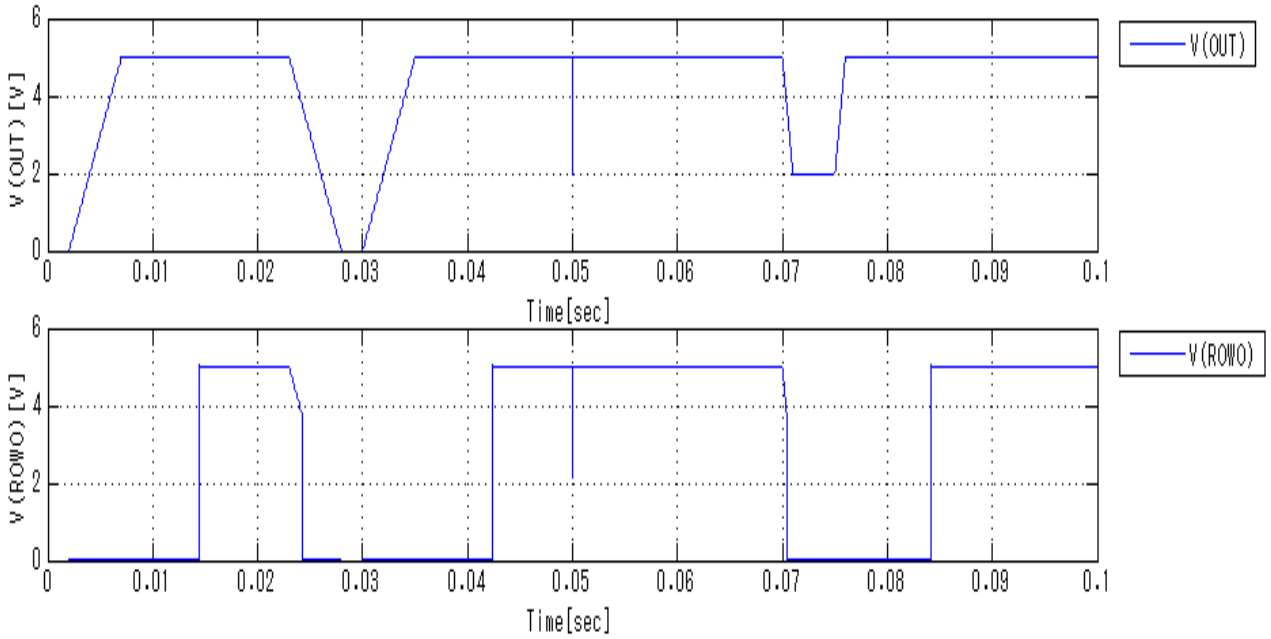
Temp. = 25deg C



Simulation results are following.
Explanatory notes — : simulated

RESET

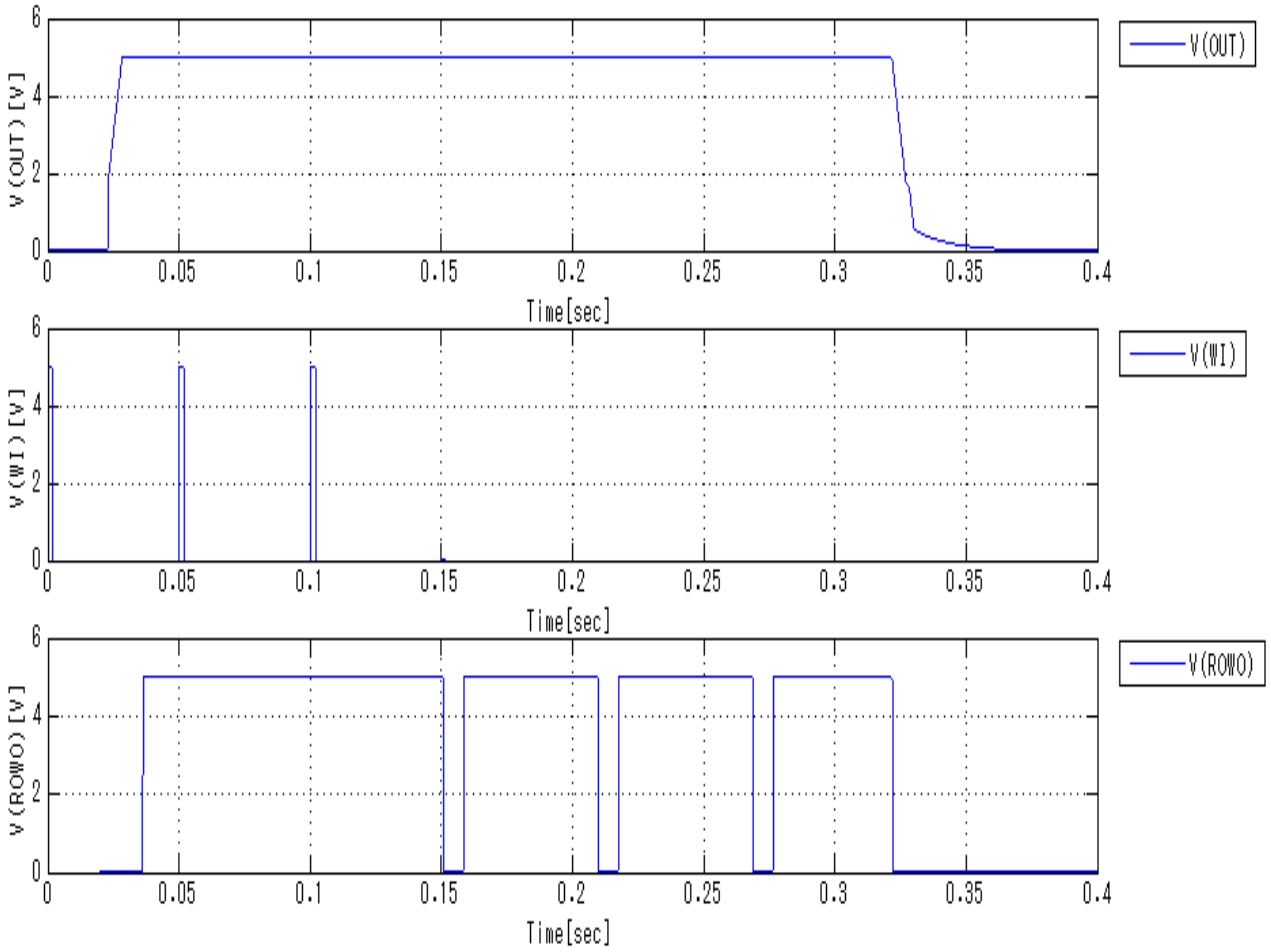
Temp. = 25deg C



Simulation results are following.
Explanatory notes — : simulated

WD

Temp. = 25deg C



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