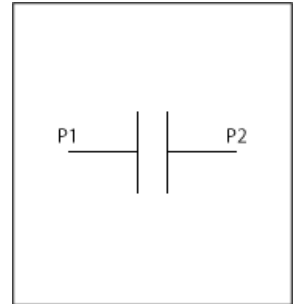


LTspice Model

Ceramic Capacitor

MURATA

GCM31CR71A226KE02



Model Information

Model An original macro model
Call Name MDC_GCM31CR71A226KE02_LT
Pin Assign 1:P1 2:P2
File List Model Library MDC_GCM31CR71A226KE02_LT01.lib
 Model Report MDC_GCM31CR71A226KE02_LT.pdf (this file)

Verified Simulator Version LTspice version XVII
Note

References

The information which was used for modeling is as follow:

[Data Sheet]

- Date/Version 2023//10/11
- Product name GCM31CR71A226KE02
- Company name Murata Manufacturing Co., Ltd.
- Characteristics CapTemp[Vf], ImpedanceFreq[Vf], ESRFreq[Vf], InductanceFreq[Vf], CapFreq[Vf], ResistanceVf[Temp]

Simulation Range

This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

Item	Range			Unit
	Min.		Max.	
Temperature	-55	to	125	deg C

Capacitor

○ : Implemented
 × : Not Implemented
 — : Not applicable

Model Functions Table

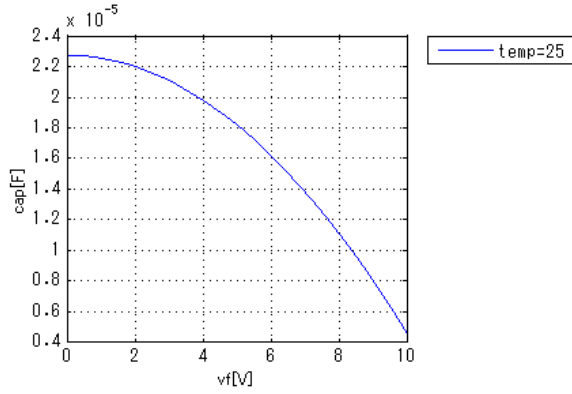
RANK=1

Functions	RANK	Implemented
Impedance-Frequency	1	○
ESR-Frequency	1	○
Capacitance-Frequency	1	○
Inductance-Frequency	1	○
Capacitance-Voltage	1	○
Capacitance-Temperature	1	○

Simulation results are following.
 Explanatory notes — : simulated

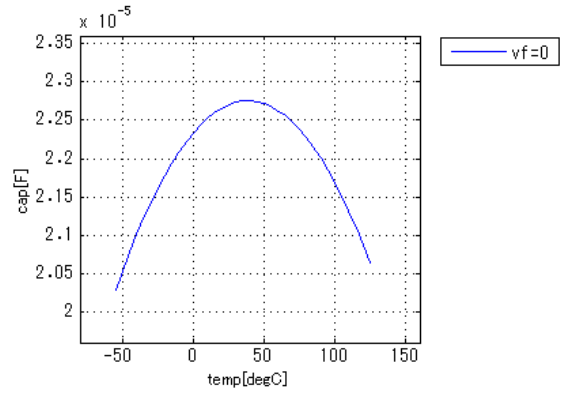
CapTemp[Vf]

Freq = 120Hz



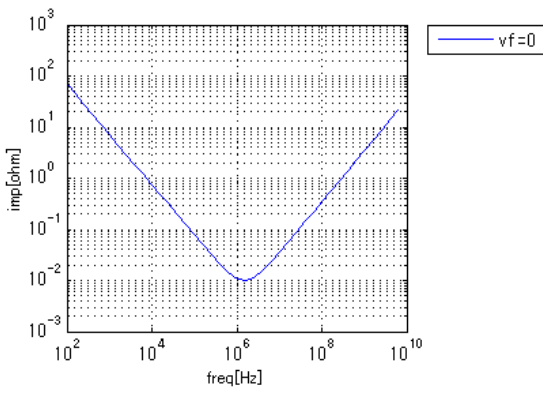
CapTemp[Vf]

Freq = 120Hz



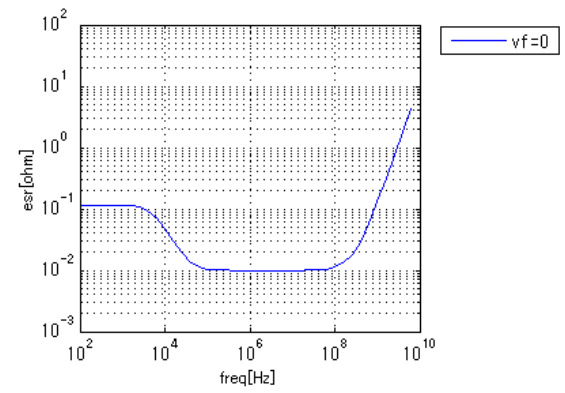
ImpedanceFreq[Vf]

temp = 25degC



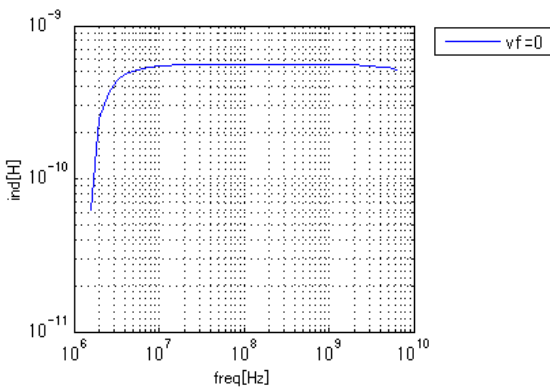
ESRFreq[Vf]

temp = 25degC



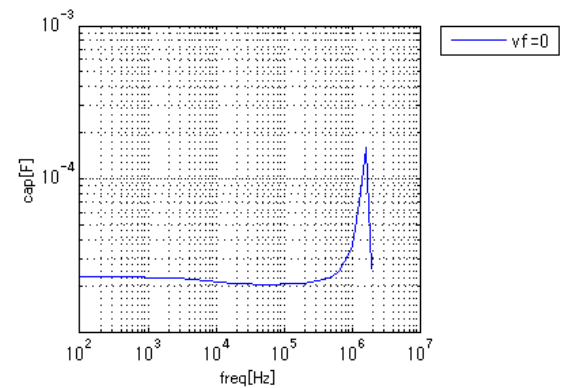
InductanceFreq[Vf]

temp = 25degC



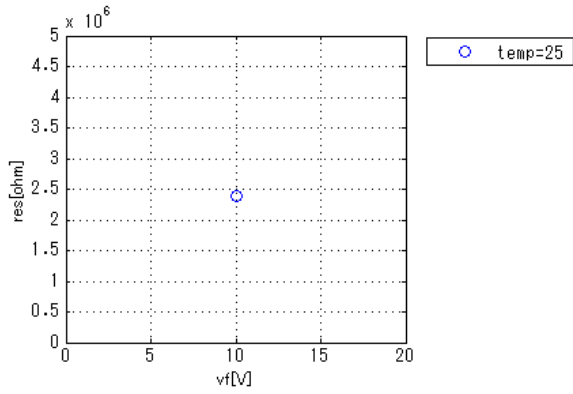
CapFreq[Vf]

Temp = 25degC



Simulation results are following.
Explanatory notes — : simulated

ResistanceVf[Temp]



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