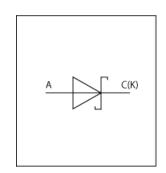


LTspice Model Schottky Diode ON NSR0230M2T5G



Model Information

Model A macro model based on general SPICE diode model

Call Name MDC_NSR0230M2T5G_LT

Pin Assign 1:C 2:A

File List Model Library MDC_NSR0230M2T5G_LT01.lib

Model Report MDC_NSR0230M2T5G_LT.pdf (this file)

Verified Simulator Version

Note

LTspice version XVII

References

The information which was used for modeling is as follow:

[Data Sheet]

Date/Version November, 2011 ? Rev. 3

Product nameCompany nameNSR0230M2T5GON Semiconductor.

● Characteristics IfVf[Temp],IrVr[Temp],CjVr,SurgeWaveform

Simulation Range

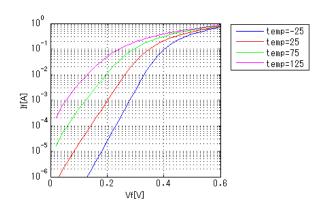
This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

Item	Range			Unit
	Min.		Max.	
Reverse Voltage	0	to	30	V
Temperature	-55	to	125	deg C

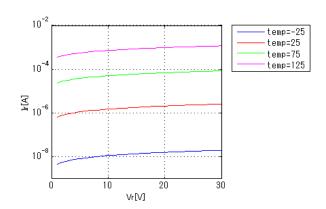


Simulation results are following. Explanatory notes — : simulated

IfVf[Temp]

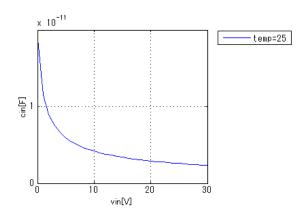


IrVr[Temp]

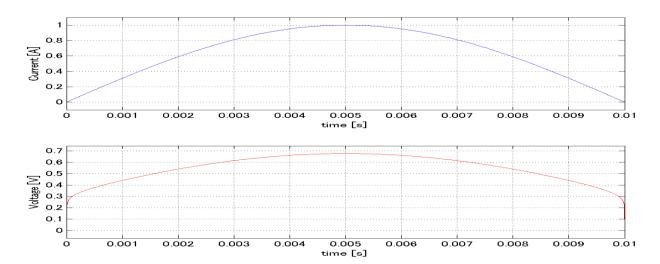


CjVr

Freq. = 1MHz



Surge Waveform (Forward)





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