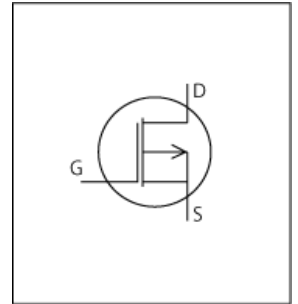


PSpice Model

PMOS

RENESAS

2SJ206-T1-AZ



Model Information

Model A macro model based on BSIM3 model
Call Name MDC_2SJ206-T1-AZ_PS
Pin Assign 1:D 2:G 3:S
File List Model Library MDC_2SJ206-T1-AZ_PS01.lib
 Model Report MDC_2SJ206-T1-AZ_PS.pdf (this file)

Verified Simulator Version PSpice version 16.6
Note

References

The information which was used for modeling is as follow:

[Data Sheet]

- Date/Version November 1994
- Product name 2SJ206-T1-AZ
- Company name Renesas Electronics Corporation
- Characteristics IdVds[Vgs], IdVgs[Temp], VthTemp[Id], Rds(on)Vgs[Id], Rds(on) Id[Vgs], IdVgs[Temp], VthTemp[Id], Rds(on)Temp[Id], Crss, Coss, Ciss, IsVsd[Temp]

Simulation Range

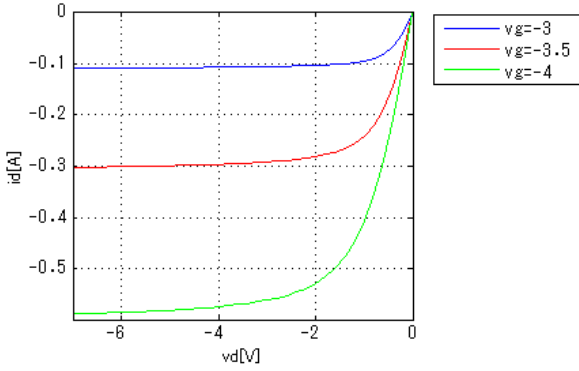
This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

Item	Range			Unit
	Min.		Max.	
Drain-source voltage (DC)	0	to	-30	V
Gate-source voltage (DC)	0	to	-20	V
Temperature	-55	to	150	deg C

Simulation results are following.
 Explanatory notes — : simulated

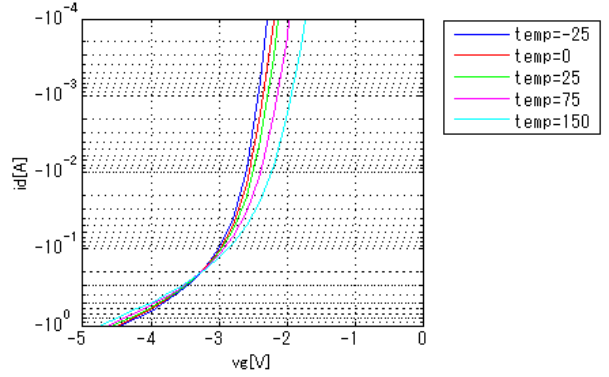
IdVds[Vgs]

Temp. = 25deg C



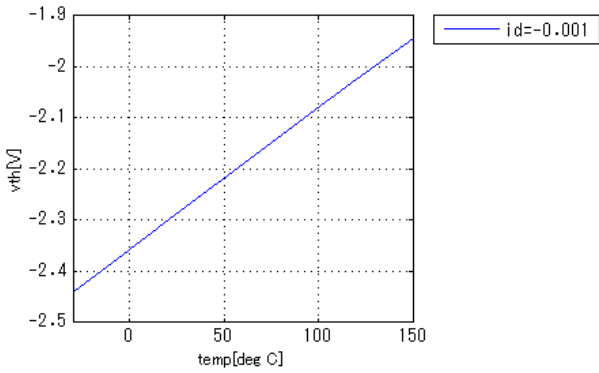
IdVgs[Temp]

$V_{ds} = -5V$

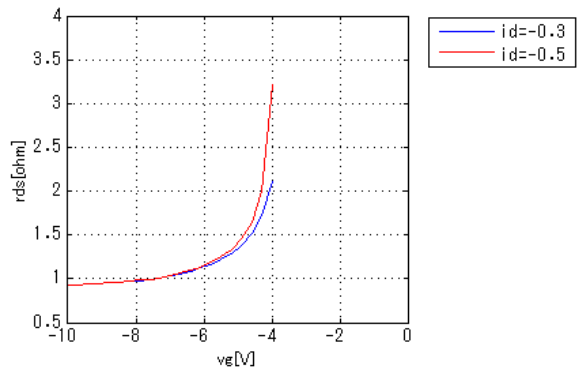


VthTemp[Id]

$V_{ds} = -5V$

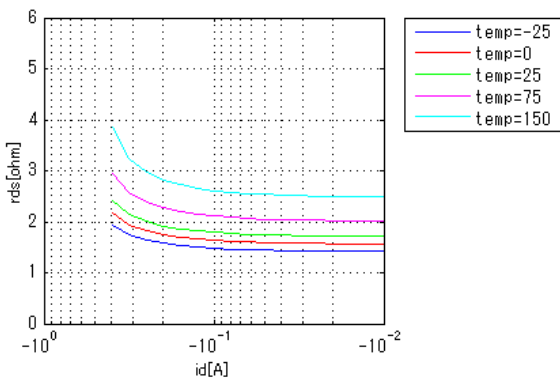


Rds(on)Vgs[Id]



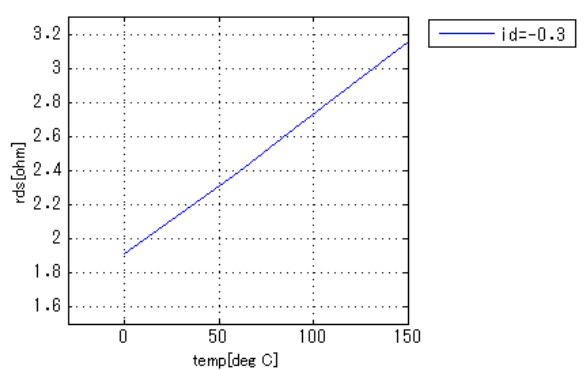
Rds(on)Id[Temp]

$V_{gs} = -4V$



Rds(on)Temp[Id]

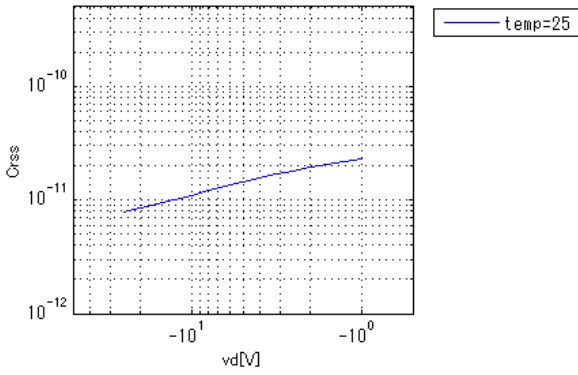
$V_{gs} = -4V$



Simulation results are following.
 Explanatory notes — : simulated

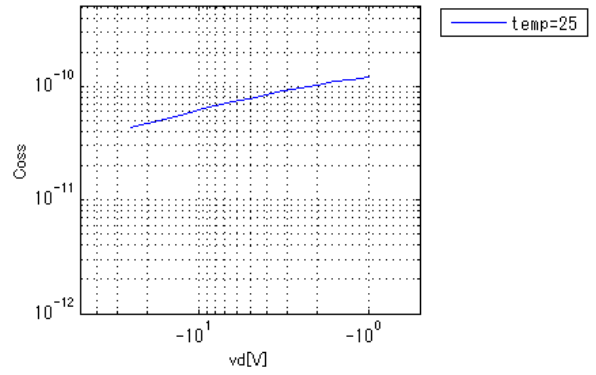
Crss

Freq. = 1MHz



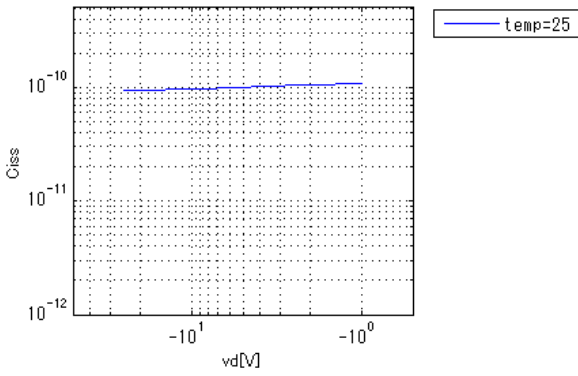
Coss

Freq. = 1MHz

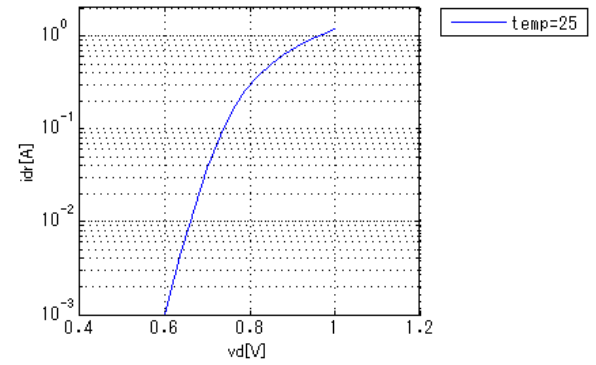


Ciss

Freq. = 1MHz



IsVsd[Temp]



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