

LTspice Model

Capacitor

NISSEY

MMTSB0050J10500000200

Model Information

Model	A macro model		
Call Name			
Pin Assign	1:1 2:2		
File List	Model Library	MDC_MMTSB0050J10500000200_LT.lib	
	Model Report	MDC_MMTSB0050J10500000200_LT.pdf(this file)	
Verified Simulator Version	LTspice XVII		

Note

References

The information which was used for modeling is as follow:

[Data Sheet]		
●Date/Version	2002.06.21 / PSC401000	
●Product name	MMT	
●Company name	NISSEI Electric Co., Ltd.	

[Characteristics listed]		
●Characteristics	Capacitance , Impedance - Frequency	

Simulation Condition

This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

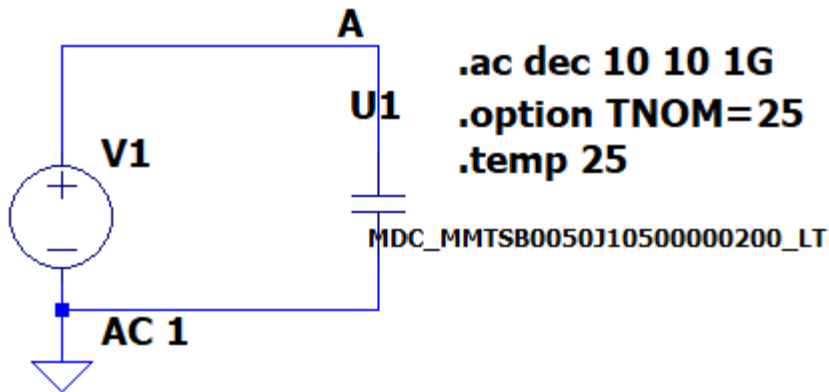
Item	Condition	Unit
Temperature	20	deg C

○ : Implemented
× : Not Implemented
— : Not applicable

Model Functions Table

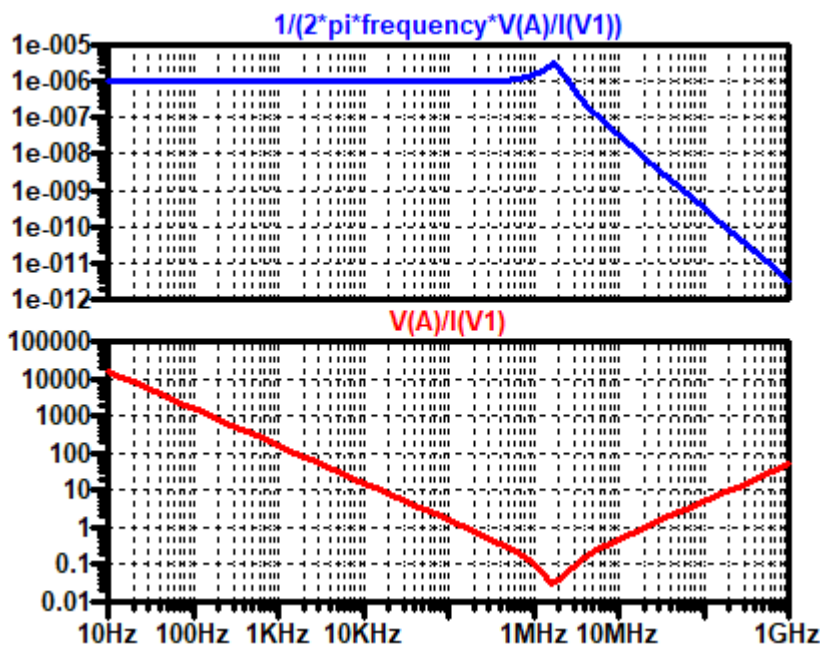
RANK=1		
Functions	RANK	Implemented
Impedance-Frequency	1	○
Capacitance-Frequency	1	○

Capacitance , Impedance-Frequency Testbench Referred to Data Sheet



Simulation results are following.
Explanatory notes — : simulated

Capacitance , Impedance-Frequency



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