

LTspice Model

LDO (Low Drop Out)

ADI

ADR361BUJZ

Model Information

Model An original macro model
Call Name MDC_ADR361BUJZ_LT
Pin Assign 1:NIC 2:GND 3:VIN 4:VOUT 5:TRIM
File List Model Library MDC_ADR361BUJZ_LT01.lib
Model Report MDC_ADR361BUJZ_LT.pdf (this file)

Verified Simulator Version LTspice version XVII
Note

References

The information which was used for modeling is as follow:

[Data Sheet]

- Date/Version Rev. E
- Product name ADR361BUJZ
- Company name Analog Devices Inc.
- Characteristics Vdiflout[Temp],Line regulation,Load regulation

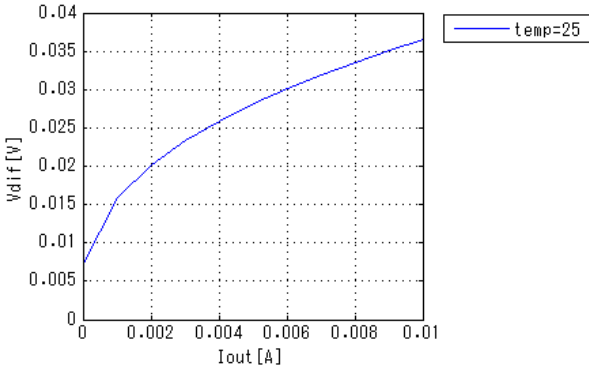
Simulation Condition

This table shows the value of evaluated simulation condition that was not occurs any convergence problems in this area.

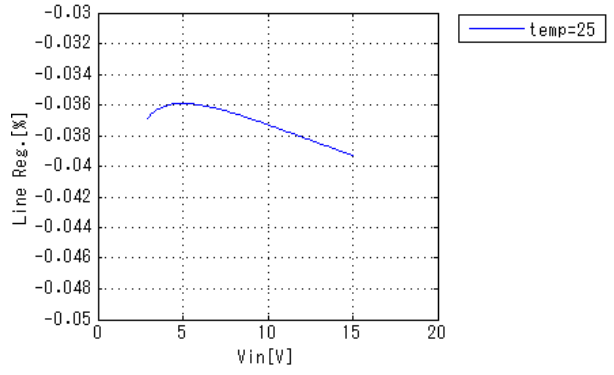
Item	Condition	Unit
Temperature	25	deg C

Simulation results are following.
 Explanatory notes — : simulated

Vdiflout[Temp]

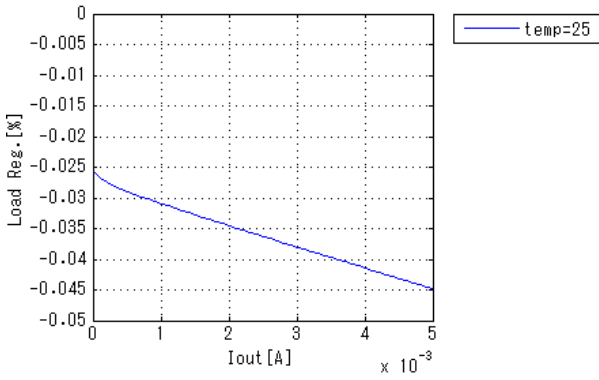


Line regulation



Load regulation

Vin = 3.5V



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