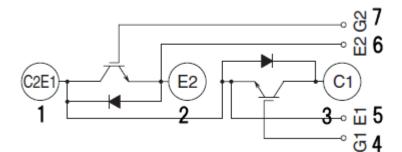


LTspice Model Nch IGBT Mitsubishi **CM300DY-24NF**



Model Information

Model	An original macro model	based on BSIM3 and Gummel-Poon model	
	MDC_CM300DY-24NF_LT		
Pin Assign	1:C2E1 2:E2 3:C1 4:G1 5:E1 6:E2 7:G2		
File List	Model Library	MDC_CM300DY-24NF_LT01.lib	
	Model Report	MDC_CM300DY-24NF_LT.pdf (this file)	

Verified Simulator Version Note

LTspice version XVII

References

The information which was used for modeling is as follow:

[Data Sheet]	
Date/Version	Feb. 2009
Product name	CM300DY-24NF
Company name	Mitsubishi Electric Corporation
Characteristics	IcVce[Vge],Vce(sat)Ic[TEMP],Vce(sat)Vge[Ic],IfVf[Temp],Ca
	pacitanceVce[Cname],SwitchingIcc[Tname],Trrlf,VgeQg[Vcc]
	,SwitchingWaveform,TrrWaveform

Simulation Range

This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

Item	Range			Unit
	Min.		Max.	
Collector-emitter voltage (DC)	0	to	1,200	V
Gate-emitter voltage (DC)	-20	to	20	V
Temperature	-40	to	125	deg C



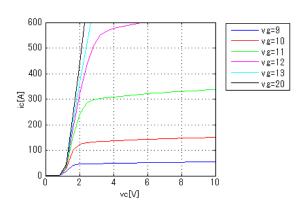
IGBT		O : Implemented × : Not Implemented — : Not applicable	
Model Functions Table	RANK=1		
Functions	RANK	Implemented	
DC Characteristics(with Temperature)	1	0	
Capacitance	1	0	
Gate Charge	1	0	
Reverse recovery characteristics	1	0	
Switching(Typ.) Inductor Load	1	0	
trr	1	0	



Simulation results are following. Explanatory notes — : simulated

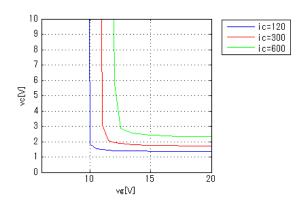
IcVce[Vge]

Temp. = 25deg C



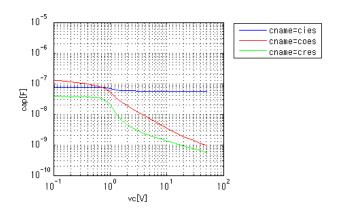
Vce(sat)Vge[lc]

Temp. = 25deg C



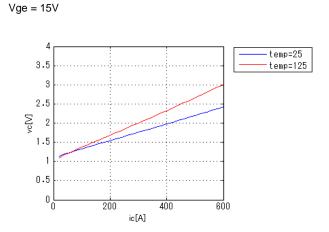
CapacitanceVce[Cname]

freq = 1000000Hz

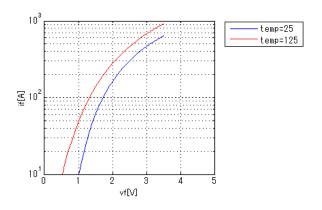


© 2022 MoDeCH Inc. LT-DIN-22-000004-1

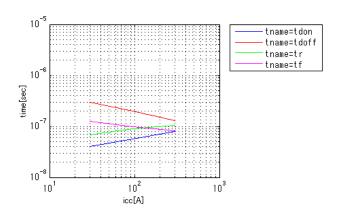
Vce(sat)lc[TEMP]



lfVf[Temp]



Switchinglcc[Tname] vgg = 15V, vcc = 600V, RGG = 10hm, Temp = 125degC

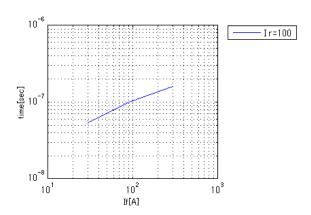




Simulation results are following. Explanatory notes — : simulated

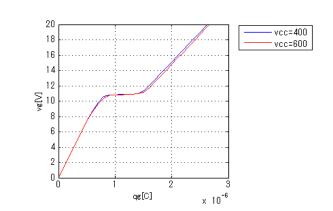
Trrlf

didt = 5000A/us, vcc = 600V



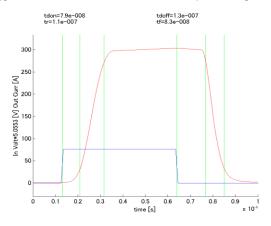
VgeQg[Vcc]





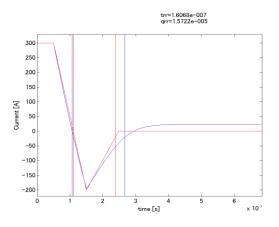
Switching Waveform (Blue : INPUT Red : OUTPUT)

vgg = 15V, vcc = 600V, RGG = 10hm, Temp = 125degC, Ic = 300A



Trr Waveform (Red : Datasheet Blue : Simulation)

didt = 5000A/us, vcc = 600V, if = 300A, ir = 200A



© 2022 MoDeCH Inc. LT-DIN-22-000004-1



DISCLAIMER

- 1. This SPICE (Simulation Program with Integrated Circuit Emphasis) model and its content (the "Contents") are copyright of MoDeCH Inc. All rights reserved. Any redistribution or reproduction of any or all part of the Contents in any form is prohibited without express written permission made by MoDeCH Inc.
- 2. MoDeCH Inc. as licensor (the" Licensor") hereby grants to you, as licensee (the "Licensee"), a nonexclusive, non-transferable license to use the Contents as long as you abide by the terms and conditions of this DISCLAIMER.
- 3. The Licensee is not authorized to sell, loan, rent and redistribute or license the Contents in whole or in part, or in modified form, to anyone.
- 4. The Licensor shall in no way be liable to the Licensee or any third party for any loss or damage (including ,but not limited to, lost profits, or other incidental, consequential, or punitive damages), however caused (including through negligence) which may be directly or indirectly suffered from, arising out of, or in connection with, any use of the Contents.
- 5. Notwithstanding anything contained in this DISCLAIMER, in no event shall Licensor be liable for any claims, damages or loss which may arise from the modification, combination, operation or use of the Contents with the Licensee's computer programs.
- 6. The Licensor does not warrant that the Contents will function in any environment.
- 7. The Contents may be changed or updated without notice. MoDeCH Inc. may also make improvements and/or changes in the products, pricing and/or the programs related to the Contents at any time without notice.



MoDeCH Inc.

Head Office Location: 5-15 Yokoyama-cho, Hachioji-Shi, Tokyo 192-0081, Japan Tel:+81-42-656-3360 E-Mail:model-on-support@modech.co.jp URL:http://www.modech.com/en/

© 2022 MoDeCH Inc. LT-DIN-22-000004-1