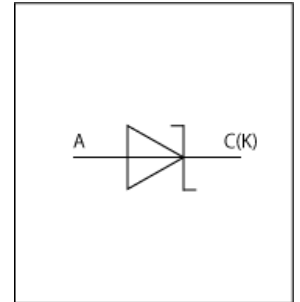


# PSpice Model

## Zener Diode

### VISHAY

### SML4755HE3



### Model Information

**Model** A macro model based on general SPICE diode model  
**Call Name** MDC\_SML4755HE3\_PS  
**Pin Assign** 1:A 2:C  
**File List** Model Library MDC\_SML4755HE3\_PS01.lib  
 Model Report MDC\_SML4755HE3\_PS.pdf (this file)

**Verified Simulator Version** PSpice version 16.6  
**Note**

### References

The information which was used for modeling is as follow:

[Data Sheet]

- Date/Version 08-Apr-15
- Product name SML4755HE3
- Company name Vishay Intertechnology, Inc.
- Characteristics IfVf[Temp],IrVr[Temp],IrVr(Breakdown)

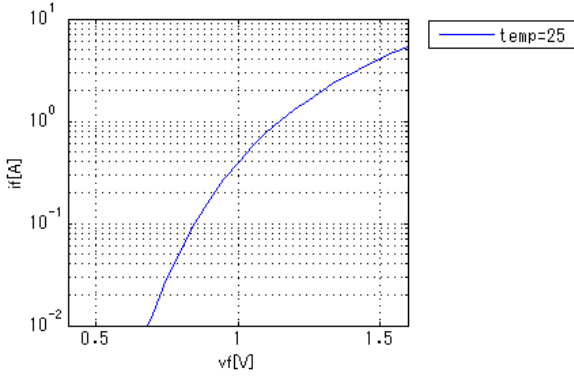
### Simulation Range

This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

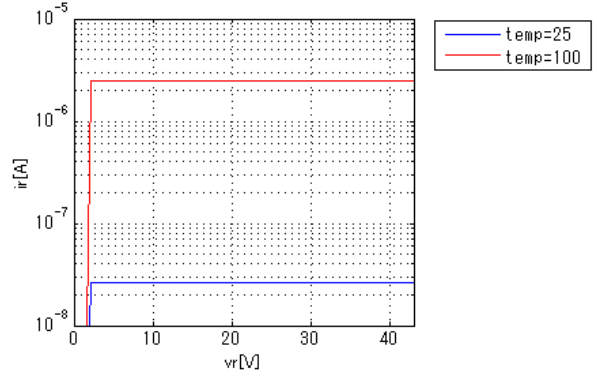
Item	Range			Unit
	Min.		Max.	
Forward Voltage	0	to	16	V
Reverse Voltage	0	to	86	V
Temperature	-65	to	150	deg C

Simulation results are following.  
 Explanatory notes — : simulated

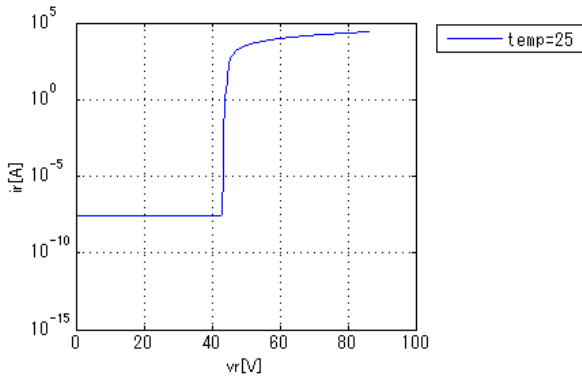
**IfVf[Temp]**



**IrVr[Temp]**



**IrVr(Breakdown)**



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