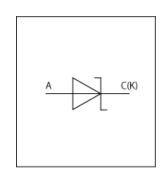


# PSpice Model Zener Diode ROHM UDZS5.6B



### **Model Information**

**Model** A macro model based on general SPICE diode model

Call Name MDC\_UDZS5\_6B\_PS

Pin Assign 1:C 2:A

File List Model Library MDC\_UDZS5\_6B\_PS02.lib

Model Report MDC\_UDZS5\_6B\_PS.pdf (this file)

Verified Simulator Version

Note

PSpice version 17.2

#### References

The information which was used for modeling is as follow:

[Data Sheet]

Date/VersionProduct nameCompany name2012.02 - Rev.DUDZS5.6BROHM Co., Ltd.

Characteristics VrIr[Temp],IrVr[Temp],CjVr,ZzIz

#### Simulation Range

This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

| Item                  | Range |    |      | Unit  |
|-----------------------|-------|----|------|-------|
|                       | Min.  |    | Max. |       |
| Zener Voltage(at 5mA) | 6     | to | 6    | V     |
| Temperature           | -55   | to | 150  | deg C |



## Diode

O: Implemented

×: Not Implemented

—: Not applicable

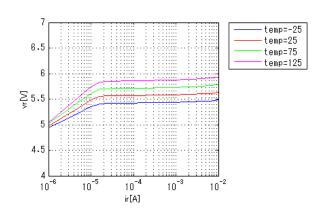
# Model Functions Table RANK=1

| Functions                             | RANK | Implemented |
|---------------------------------------|------|-------------|
| IF-VF(Temp)                           | 1    | _           |
| IR-VR(Temp)                           | 1    | 0           |
| Capacitance                           | 1    | 0           |
| Reverse recovery characteristics      | 1    | _           |
| Zz-Iz                                 | 1    | 0           |
| Rectification characteristics(Bridge) | 1    | _           |
| Surge voltage-Transient               | 1    | _           |

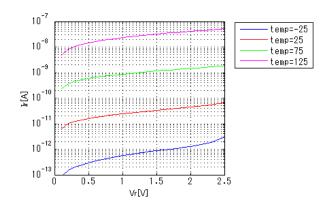


Simulation results are following. Explanatory notes — : simulated

## VrIr[Temp]

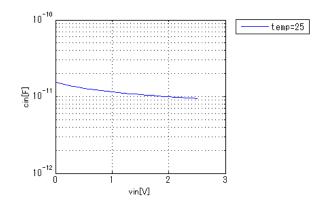


## IrVr[Temp]



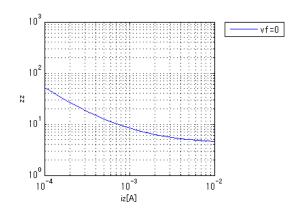
#### CjVr

Freq = 1000000Hz



#### Zzlz

izl = 0.885377, izh = 1.1146236





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