

MDC_DSA7102_PS

PSpice Model PNP Panasonic DSA7102

Model Information

ModelMextram modelCall NameMDC_DSA7102_PSPin Assign1:C 2:B 3:EFile ListModel Library
Model Report

MDC_DSA7102_PS01.lib MDC_DSA7102_PS.pdf (this file)

Verified Simulator Version Note

PSpice version 16.6

References

The information which was used for modeling is as follow:

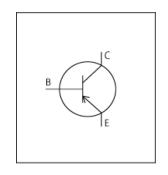
[Data Sheet] • Date/Version • Product name • Company name • Characteristics

2014/02 DSA7102 Panasonic Corporation IcVce[ib],hFEIc[Temp],Vce(sat)Ic[Temp],IcVbe[Temp],Cob,fT Ic[Vce]

Simulation Range

This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

Item	Range			Unit
	Min.		Max.	
Collector-emitter voltage (DC)	0	to	-80	V
Base current (DC)	0	to	-0.10	А
Temperature	-40	to	85	deg C

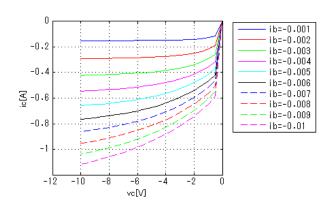




Simulation results are following. Explanatory notes — : simulated

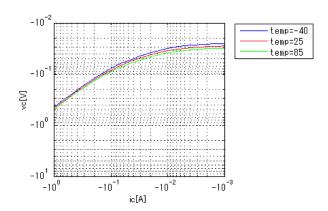
IcVce[ib]

Temp. = 25deg C

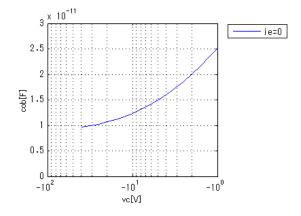


Vce(sat)lc[Temp]

IC/IB = 10



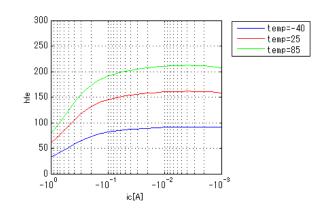




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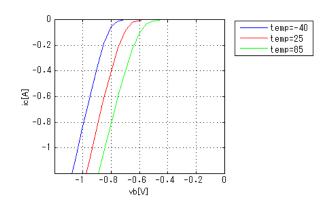
hFElc[Temp]

Vce = -2V

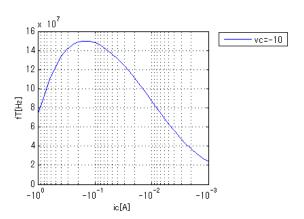


IcVbe[Temp]

Vce = -10V









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