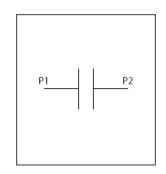


PSpice Model Ceramic Capacitor MURATA GRM155C80J224KE01



Model Information

Model An original macro model

Call Name MDC GRM155C80J224KE01 PS

Pin Assign 1:P1 2:P2

File List Model Library MDC_GRM155C80J224KE01_PS01.lib

Model Report MDC_GRM155C80J224KE01_PS.pdf (this file)

Verified Simulator Version

Note

PSpice version 16.6

References

The information which was used for modeling is as follow:

[Measured]

● Date 6/20/2016

Company nameCharacteristicsMurata Manufacturing Co., Ltd.Cap.Freq.,Imp.Freq.,EsrFreq.

Simulation Range

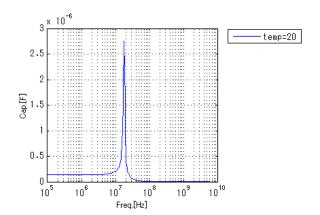
This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

Item	Range			Unit
	Min.		Max.	
Frequency	100,000	to	6,000,000,000	Hz
Temperature	20	to	20	deg C

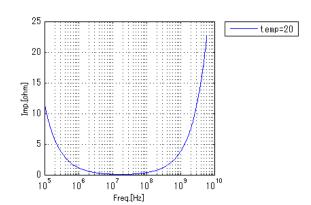


Simulation results are following. Explanatory notes — : simulated

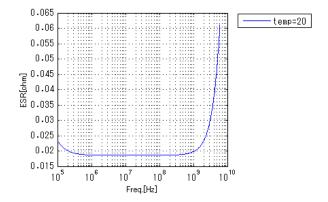
Cap.Freq.



Imp.Freq.



EsrFreq.





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