

PSpice Model Photocoupler Tr. Output TOSHIBA TLP785

Model Information

	An original macro model MDC_TLP785_PS			
Pin Assign	1:Anode 2:Cathode 3:Emitter 4:Collector			
File List	Model Library	MDC_TLP785_PS01.lib		
	Model Report	MDC_TLP785_PS.pdf (this file)		

Verified Simulator Version Note

PSpice version 16.6

References

The information which was used for modeling is as follow:

[Data Sheet]	
Date/Version	2015/10/9
Product name	TLP785
Company name	Toshiba Corporation
Characteristics	IfVf[Temp],IcVce[If],IcVce[If]2,IcIf[Vce],IcTemp[If],SwitchingTime,SwitchingTime2,SwitchingTime3
1 2	

Simulation Range

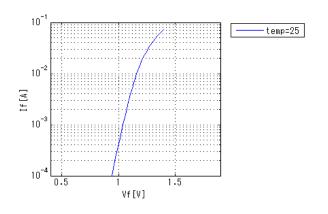
This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

Item	Range			Unit
	Min.		Max.	
Forward current (DC)	0	to	0.06	А
Collector-emitter voltage (DC)	0	to	80	V
Temperature	-55	to	125	deg C



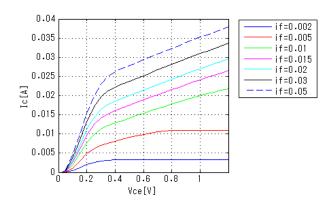
Simulation results are following. Explanatory notes -: simulated

lfVf[Temp]



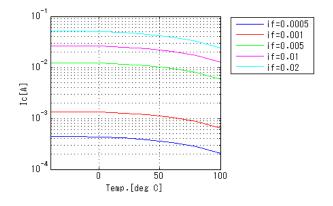
IcVce[lf]2

Temp. = 25deg C



IcTemp[If]

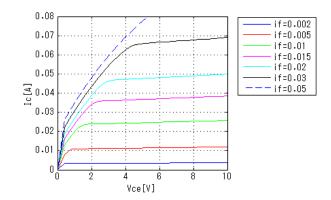
Vc = 5V



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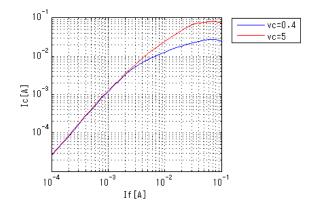
lcVce[lf]

Temp. = 25deg C



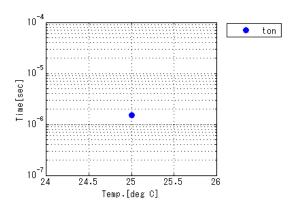
Iclf[Vce]

Temp. = 25deg C



SwitchingTime

+If = 0.016A, -If = 0A, Vc = 5V, RI = 1900ohm

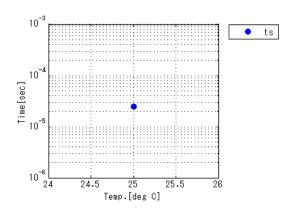




Simulation results are following. Explanatory notes -: simulated

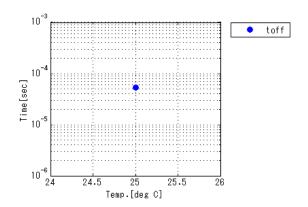
SwitchingTime2

+If = 0.016A, -If = 0A, Vc = 5V, RI = 1900ohm



SwitchingTime3

+If = 0.016A, -If = 0A, Vc = 5V, RI = 1900ohm





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MoDeCH Inc.

Head Office Location: Mitsuiseimei Hachioji Bldg., 5-15 Yokoyama-cho, Hachioji-Shi, Tokyo 192-0081, Japan Tel:+81-42-656-3360 E-Mail:model-on-support@modech.co.jp URL:http://www.modech.com/en/