

PSpice Model Aluminum Electrolytic Capacitor Rubycon 35TZV22M5X6.1

Model Information

Model	A macro mode	l	
Call Name	MDC_35TZV22M5X6_1_PS		
Pin Assign	No polarity		
File List	Model Library	MDC_35TZV22M5X6_1_PS01.lib	
	Model Report	MDC_35TZV22M5X6.1_PS.pdf(this file)	
Verified Simulator Version		PSpice 17.2	

Note

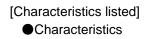
References

The information which was used for modeling is as follow:

[Data Sheet]

Date/Version			
Product name			
Company name			

N.A. 35TZV22M5X6.1 Rubycon Corporation



ESR vs Freqency Impeadance vs Freqency

Simulation Condition

This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

Item	Condition	Unit
Temperature	20	deg C



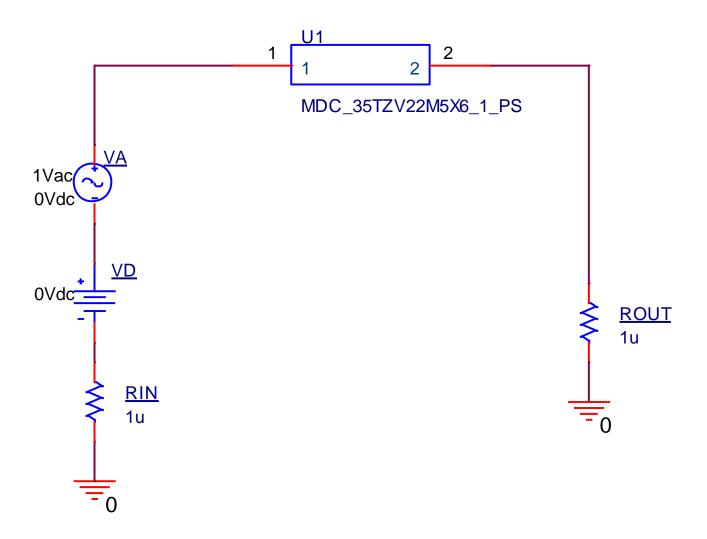
Model Functions Table

Functions	Implemented	
ESR vs Freqency	0	
Impeadance vs Freqency	0	



ESR vs Freqency Testbench

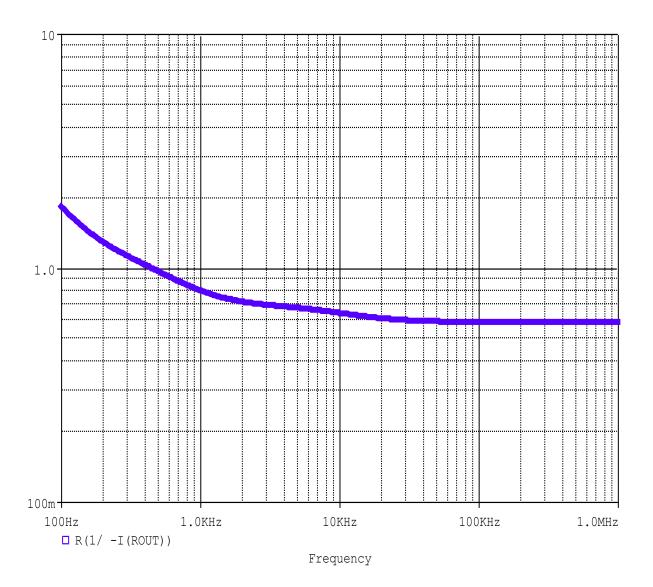
Referred to Data Sheet





Simulation results are following. Explanatory notes -: simulated

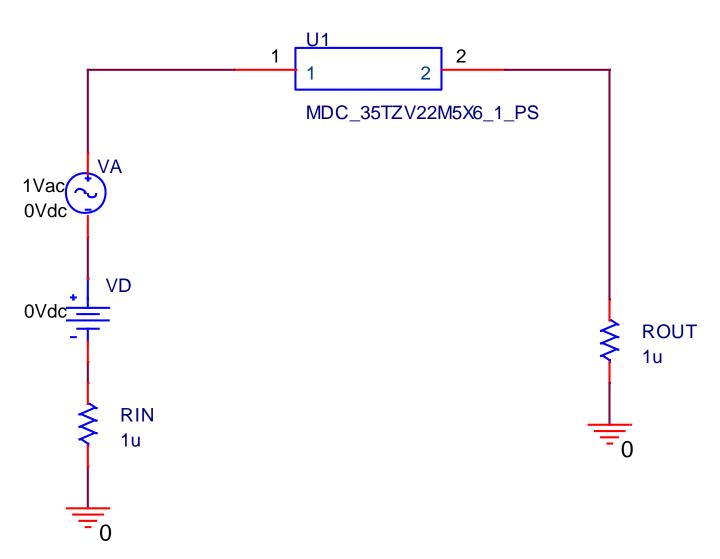
ESR vs Freqency





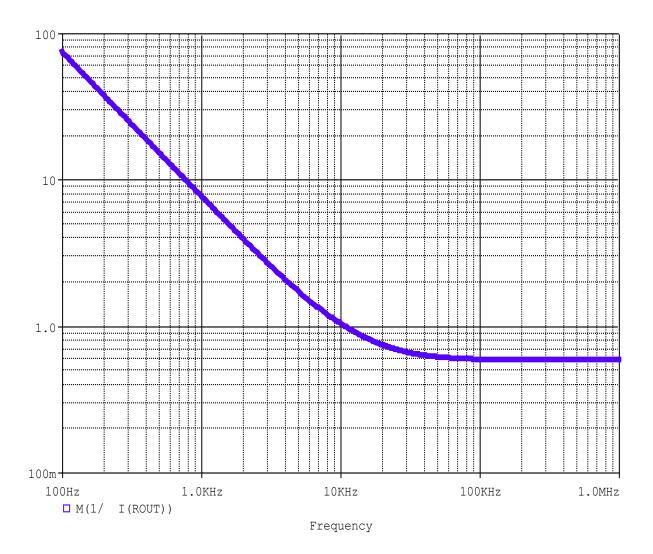
Impeadance vs Freqency Testbench

Referred to Data Sheet





Impeadance vs Freqency





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