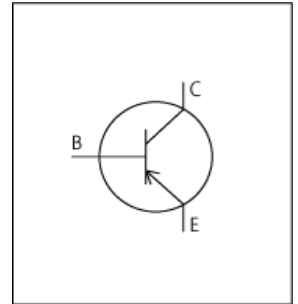


LTspice Model

PNP

Isahaya

2SA1944-T111-1G



Model Information

Model Gummel-Poon model
Call Name MDC_2SA1944-T111-1G_LT
Pin Assign 1:C 2:B 3:E
File List Model Library MDC_2SA1944-T111-1G_LT01.lib
 Model Report MDC_2SA1944-T111-1G_LT.pdf (this file)

Verified Simulator Version LTspice version XVII
Note

References

The information which was used for modeling is as follow:

[Data Sheet]

- Date/Version Unknown
- Product name 2SA1944-T111-1G
- Company name Isahaya Electronics Corporation
- Characteristics $I_{cV_{ce}[ib]}$, $I_{cV_{be}[Temp]}$, $h_{FEIc}[Temp]$, $V_{ceIb}[Ic]$, $f_{Tle}[V_{ce}]$, C_{ob} , $V_{ce(sat)Ic}[Temp]$

Simulation Range

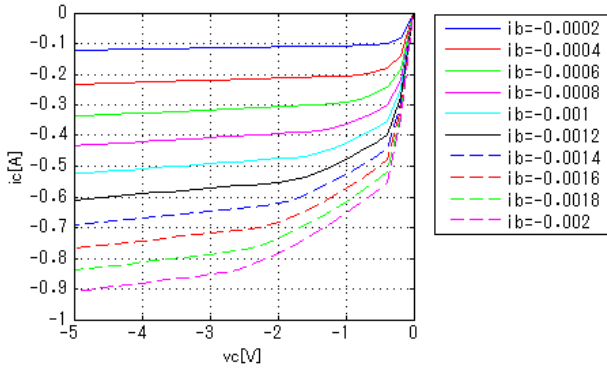
This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

Item	Range			Unit
	Min.		Max.	
Collector-emitter voltage (DC)	0	to	-50	V
Collector current (DC)	0	to	-1	A
Temperature	-55	to	150	degC

Simulation results are following.
 Explanatory notes — : simulated

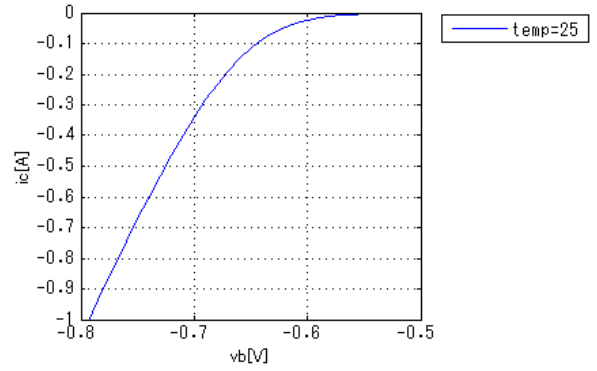
IcVce[ib]

Temp. = 25deg C



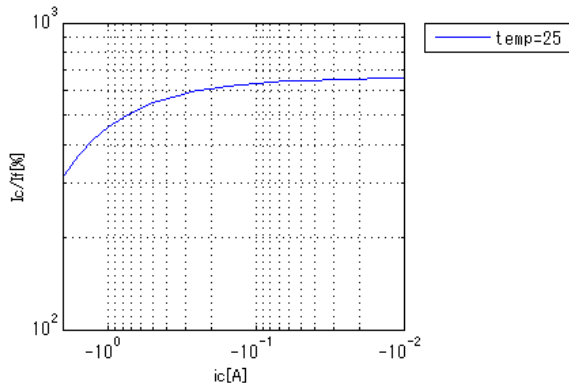
IcVbe[Temp]

Vce = -6V

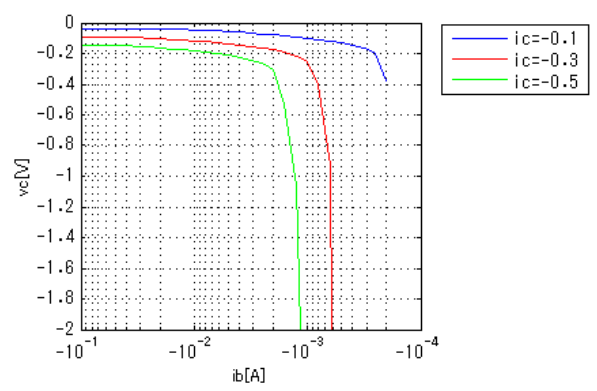


hFEIc[Temp]

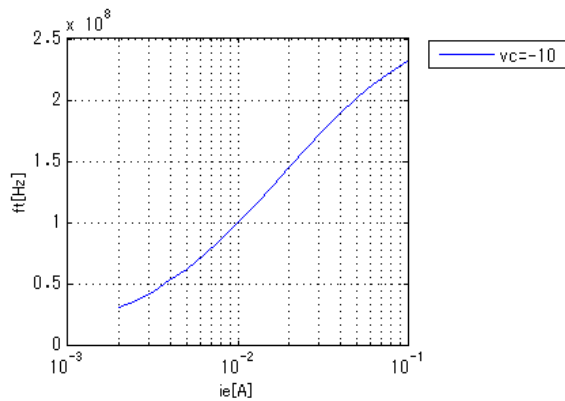
Vce = -6V



Vcelb[Ic]

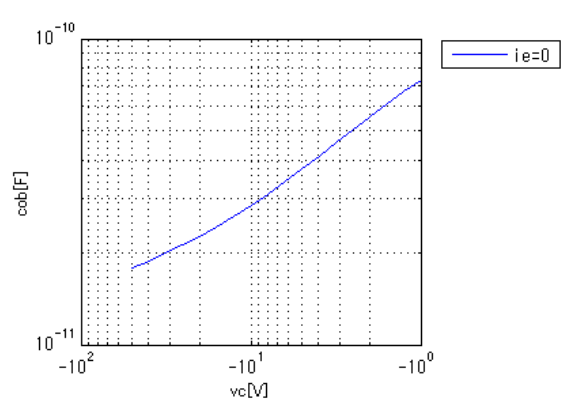


fTle[Vce]



Cob

Freq. = 1MHz

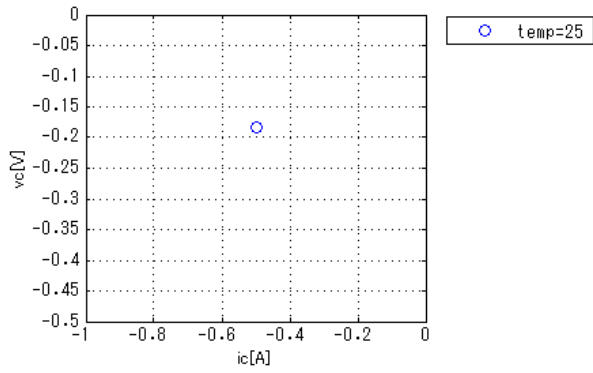


Simulation results are following.

Explanatory notes — : simulated

Vce(sat)Ic[Temp]

IC/IB = 50



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