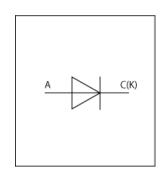


PSpice Model Fast Recovery Diode SHINDENGEN SG5L20USM



Model Information

Model A macro model based on general SPICE diode model

Call Name MDC_SG5L20USM_PS

Pin Assign 1:C 2:A

File List Model Library MDC_SG5L20USM_PS01.lib

Model Report MDC_SG5L20USM_PS.pdf (this file)

Verified Simulator Version

Note

PSpice version 17.2

References

The information which was used for modeling is as follow:

[Data Sheet]

Date/Version 2010.06Product name SG5L20USM

◆Company name Shindengen Electric Manufacturing Co., Ltd.

Characteristics IfVf[Temp],CjVr,IrVr[Temp],TrrlfIr,TrrWaveform,SurgeWavef

orm

Simulation Range

This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

Item	Range			Unit
	Min.		Max.	
Reverse Voltage	0	to	200	V
Temperature	-55	to	150	deg C



Model Functions Table

Diode

O: Implemented

×: Not Implemented

—: Not applicable

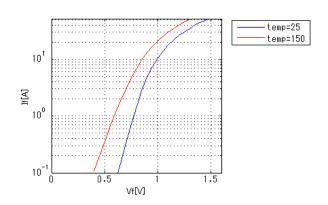
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	_		` —	

	10 (1410 ±	
Functions	RANK	Implemented
IF-VF(Temp)	1	0
IR-VR(Temp)	1	0
Capacitance	1	0
Reverse recovery characteristics	1	0
Zz-Iz	1	_
Rectification characteristics(Bridge)	1	_
Surge cullent-Transient	1	0



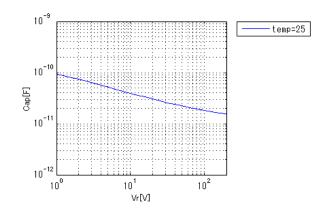
Simulation results are following. Explanatory notes — : simulated

IfVf[Temp]

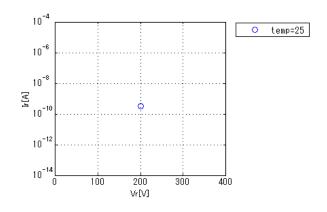


CjVr

Freq = 1000000Hz

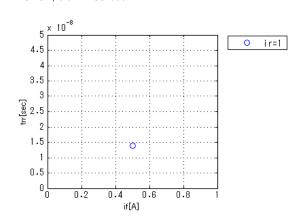


IrVr[Temp]



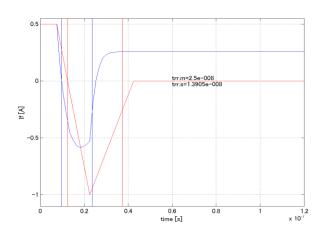
Trrlflr

irr = 0.25A, didt = 100A/us



Trr Waveform (Red: Datasheet Blue: Simulation)

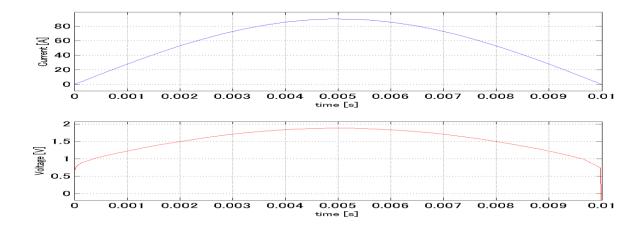
didt = 100A/us, if = 0.5A, ir = 1A, irr = 0.25A





Simulation results are following. Explanatory notes — : simulated

Surge Current Waveform (Forward Sine Half 50Hz)





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