

## PSpice Model Rectifier Diode ON FS8K

### **Model Information**

ModelA macro model based on general SPICE diode modelCall NameMDC\_FS8K\_PSPin Assign1:A 2:CFile ListModel LibraryMDC\_FS8K\_PS01.libModel ReportMDC\_FS8K\_PS.pdf (this file)

Verified Simulator Version Note

PSpice version 16.6

#### References

The information which was used for modeling is as follow:

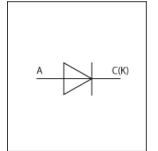
[Data Sheet]

Rev 1.0
FS8K
ON Semiconductor.
lfVf[Temp],IrVr[Temp],CjVr,Trr

#### **Simulation Range**

This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

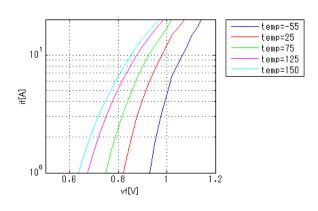
Item	Range			Unit
	Min.		Max.	
Forward Voltage	0	to	11.5	V
Temperature	-55	to	150	deg C





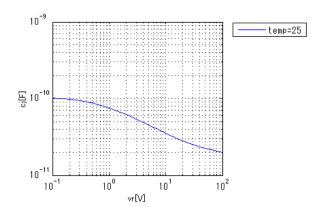
# Simulation results are following. Explanatory notes -: simulated

#### lfVf[Temp]

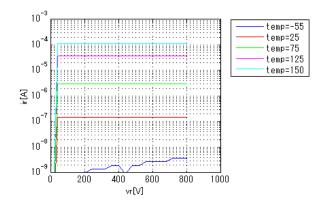


CjVr

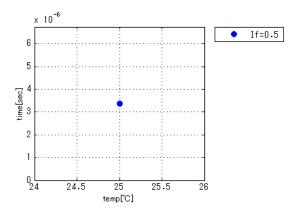
Freq. = 1MHz



IrVr[Temp]



Trr





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