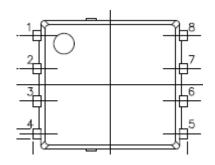


PSpice Model NMOS RENESAS NP75N04YLG

1, 2, 3 : Source 4 : Gate 5, 6, 7, 8: Drain



Model Information

Model A macro model based on BSIM3 model

Call Name MDC_NP75N04YLG_PS

Pin Assign 1:S 2:S 3:S 4:G 5:D 6:D 7:D 8:D

File List Model Library MDC_NP75N04YLG_PS01.lib

Model Report MDC_NP75N04YLG_PS.pdf (this file)

Verified Simulator Version

Note

PSpice version 17.2

References

The information which was used for modeling is as follow:

[Data Sheet]

Date/VersionProduct nameMar 02, 2015NP75N04YLG

Company name Renesas Electronics Corporation

●Characteristics IdVds[Vgs],IdVgs[Temp],VthTemp[Id],YfsId[Temp],Rds(on)Id

[Vgs],Rds(on)Vgs[Id],CapacitanceVds[Cname],SwitchingIdd[

Tname],VgsQg[Vdd],VdsQg[Vdd],IsVsd[Vgs],Trrlf[Ir],Switchi

ngWaveform,TrrWaveform

Simulation Range

This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

Item	Range			Unit
	Min.		Max.	
Drain-source voltage (DC)	0	to	40	V
Gate-source voltage (DC)	-20	to	20	V
Temperature	-55	to	175	deg C



Model Functions Table

MOSFET

O: Implemented

×: Not Implemented

—: Not applicable

RANK=1

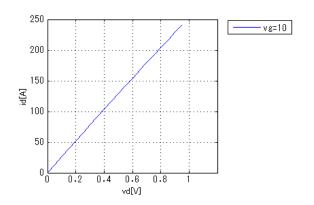
	KANK-1	
Functions	RANK	Implemented
ID-VDS-VGS	1	0
ID-VGS(Temp)	1	0
RDS(on)	1	0
Capacitance	1	0
Gate Charge	1	0
IS-VSD(Forward)	1	0
Reverse recovery	1	0
Switching(Typ.)	1	0
Bv	1	_
Yfs	1	0
Vth	1	0



Simulation results are following. Explanatory notes — : simulated

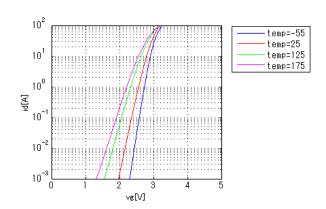
IdVds[Vgs]

Temp = 25degC



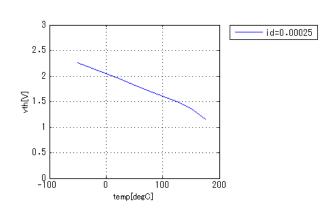
IdVgs[Temp]

Vds = 10V



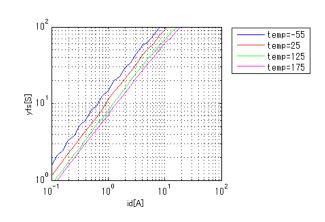
VthTemp[Id]

Vd = Vg



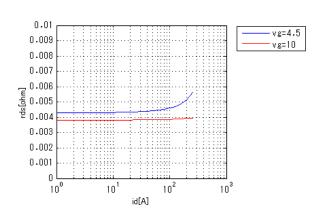
Yfsld[Temp]

Vds = 5V



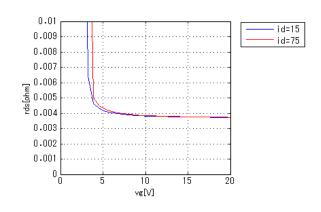
Rds(on)Id[Vgs]

Temp = 25degC



Rds(on)Vgs[ld]

Temp = 25degC

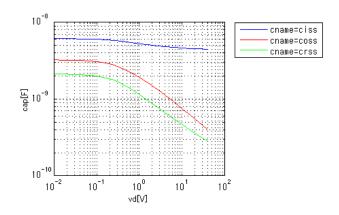




Simulation results are following. Explanatory notes — : simulated

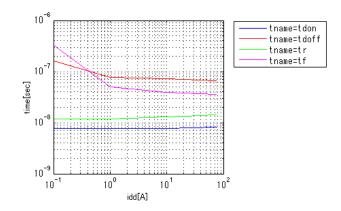
CapacitanceVds[Cname]

freq = 1000000Hz



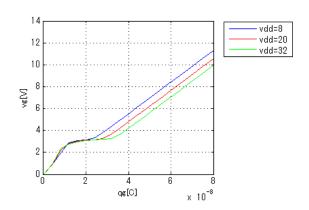
SwitchingIdd[Tname]

vgg = 10V, vdd = 20V, RGG = 0.10hm



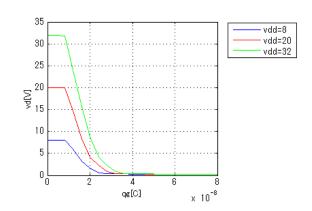
VgsQg[Vdd]

Id = 75A

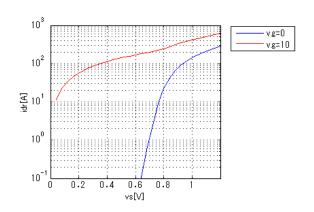


VdsQg[Vdd]

Id = 75A

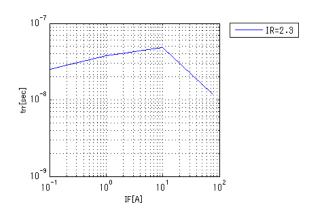


IsVsd[Vgs]



Trrlf[lr]

vdd = 20V, didt = 100A/us, Temp = 25degC

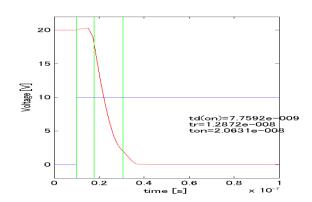


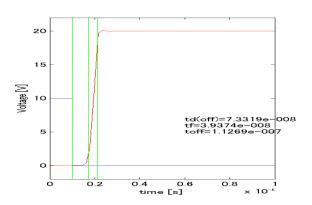


Simulation results are following. Explanatory notes — : simulated

Switching Waveform (Blue : INPUT Red : OUTPUT)

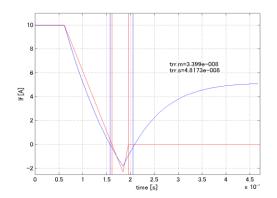
vgg = 10V, vcc = 20V, RGG = 0.10hm, Temp = 25degC, Ic = 10A





Trr Waveform (Red : Datasheet Blue : Simulation)

didt = 100A/us, vcc = 20V, if = 10A, ir = 2.3A





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