

MDC_2SD1782K_LT

LTspice Model NPN ROHM 2SD1782K

Model Information

ModelGummel-Poon modelCall NameMDC_2SD1782K_LTPin Assign1:E 2:B 3:CFile ListModel Library
Model Report

MDC_2SD1782K_LT01.lib MDC_2SD1782K_LT.pdf (this file)

Verified Simulator Version Note

LTspice version XVII

References

The information which was used for modeling is as follow:

 [Data Sheet]

 Date/Version
 Product name
 Company name
 Characteristics

 20160909 - Rev.003

 2SD1782K

 ROHM Co., Ltd.

 IcVbe[Temp],IcVce[ib],hFEIc[Temp],Vce(sat)Ic[hFE],Vce(sat)

 Ic[Temp],Vce(sat)Ic[Temp]2,Vce(sat)Ic[Temp]3,fTIe[Vce],Co

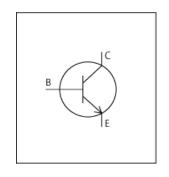
 b,Cib

 Date/Version

Simulation Range

This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

Item	Range			Unit
	Min.		Max.	
Collector-emitter voltage (DC)	0	to	80	V
Base-emitter voltage (DC)	0	to	5	V
Temperature	-55	to	150	deg C





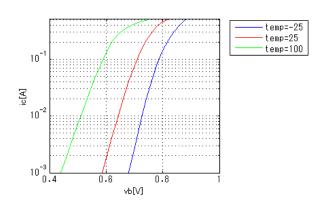
BJT		O : Implemented × : Not Implemented	
Model Functions Table	-: Not applicable		
Functions	RANK	Implemented	
IC-VBE(Temp)	1	0	
IC-VCE-IB(Temp)	1	0	
IC-hFE(Temp)	1	0	
VCE(sat)-IC	1	0	
VBE(sat)-IB	1	—	
Capacitance	1	0	
Transition Frequency	1	0	



Simulation results are following. Explanatory notes — : simulated

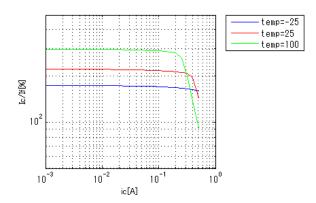
IcVbe[Temp]

Vce = 3V



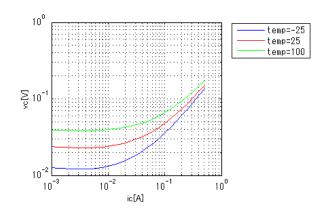
hFElc[Temp]

Vce = 3V



Vce(sat)lc[Temp]

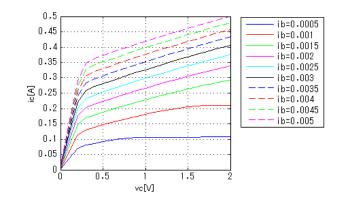
IC/IB = 10



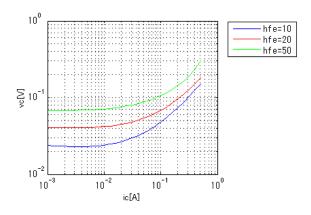
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lcVce[ib]

Temp. = 25degC

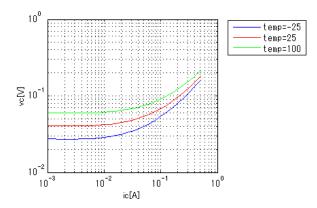


Vce(sat)lc[hFE]



Vce(sat)lc[Temp]2

IC/IB = 20

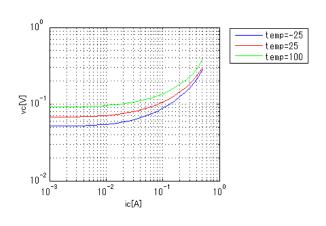




Simulation results are following. Explanatory notes — : simulated

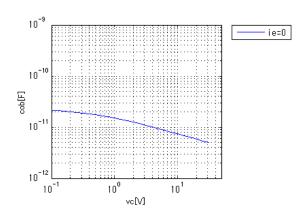
Vce(sat)lc[Temp]3

IC/IB = 50

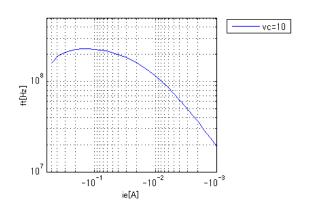


Cob

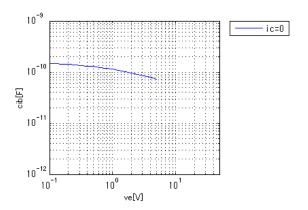
Freq. = 1MHz



fTle[Vce]









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