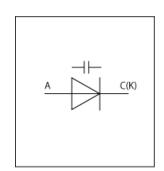


PSpice Model Variable Capacitance Diode NXP BBY40



Model Information

Model A macro model based on general SPICE diode model

Call Name MDC_BBY40_PS

Pin Assign 1:A 2:C

File List Model Library MDC_BBY40_PS01.lib

Model Report MDC_BBY40_PS.pdf (this file)

Verified Simulator Version

Note

PSpice version 16.6

References

The information which was used for modeling is as follow:

[Data Sheet]

Date/VersionProduct nameBBY40

Company name NXP Semiconductors N.V.

Characteristics CiVr

Simulation Range

This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

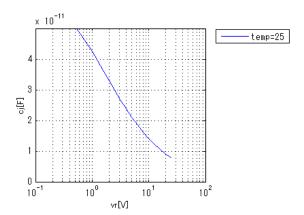
Item	Range			Unit
	Min.		Max.	
Reverse Voltage	0	to	60	V
Temperature	-55	to	150	deg C



Simulation results are following. Explanatory notes — : simulated

CjVr

Freq. = 1MHz





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