

# LTspice Model

## Capacitor

### Murata

## GRM1882C1H101JA01D

### Model Information

<b>Model</b>	A macro model	
<b>Call Name</b>	MDC_GRM1882C1H101JA01D_LT	
<b>Pin Assign</b>	1:1 2:2	
<b>File List</b>	Model Library	MDC_GRM1882C1H101JA01D_LT.lib
	Model Report	MDC_GRM1882C1H101JA01D_LT.pdf(this file)
<b>Verified Simulator Version</b>	LTspice XVII	

### Note

### References

The information which was used for modeling is as follow:

[Data Sheet]

- Date/Version
- Product name GRM1882C1H101JA01D
- Company name Murata Manufacturing Co., Ltd.

[Characteristics listed]

- Characteristics Capacitance , Impedance - Frequency

### Simulation Condition

This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

Item	Condition	Unit
Temperature	20	deg C

○ : Implemented  
 × : Not Implemented  
 — : Not applicable

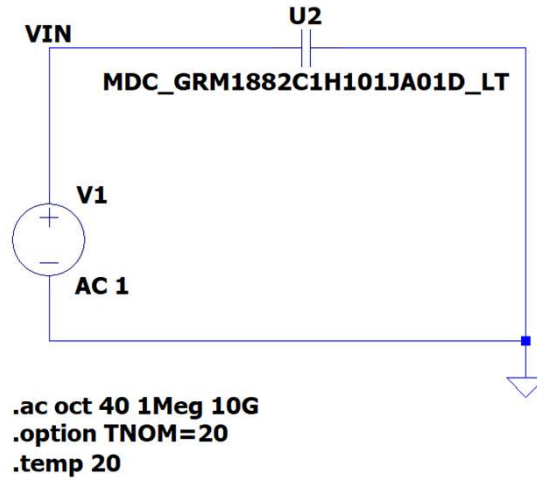
**Model Functions Table**

**RANK=1**

Functions	RANK	Implemented
Impedance-Frequency	1	○
Capacitance-Frequency	1	○

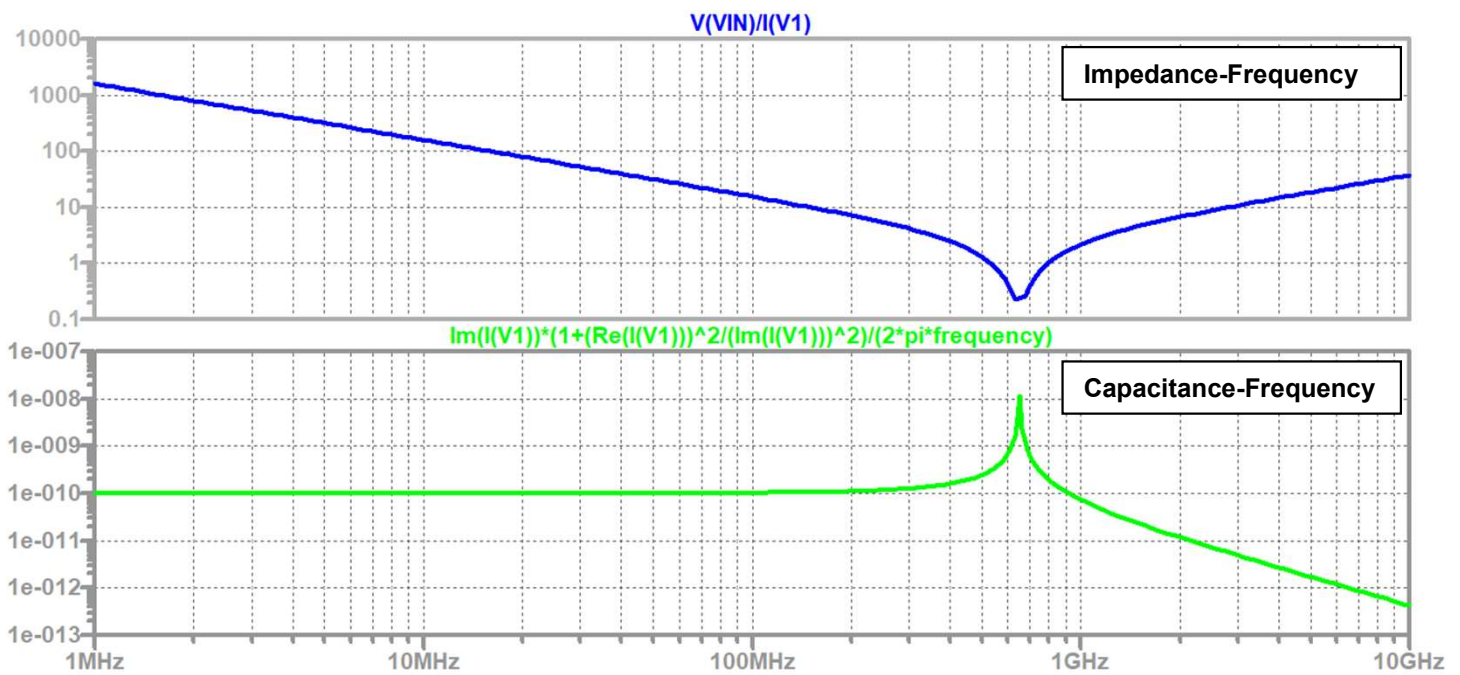
Capacitance , Impedance-Frequency Testbench

Referred to Data Sheet



Simulation results are following.  
 Explanatory notes — : simulated

Capacitance , Impedance-Frequency



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