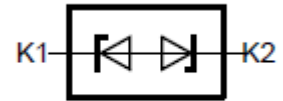


LTspice Model

ESD Protection Diode

Nexperia

PESD18VF1BL



Model Information

Model A macro model based on general SPICE diode model
Call Name MDC_PESD18VF1BL_LT
Pin Assign 1:K1 2:K2
File List Model Library MDC_PESD18VF1BL_LT01.lib
 Model Report MDC_PESD18VF1BL_LT.pdf (this file)

Verified Simulator Version LTspice version XVII
Note

References

The information which was used for modeling is as follow:

[Data Sheet]

- Date/Version 11 April 2023
- Product name PESD18VF1BL
- Company name Nexperia B.V.
- Characteristics CjVr,TipVclpp,TipWaveform,SurgeCurrentWaveform,SurgeGunWaveform

Simulation Range

This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

Item	Range			Unit
	Min.		Max.	
Reverse Voltage	0	to	18	V
Temperature	-65	to	150	deg C

Diode

○ : Implemented
× : Not Implemented
— : Not applicable

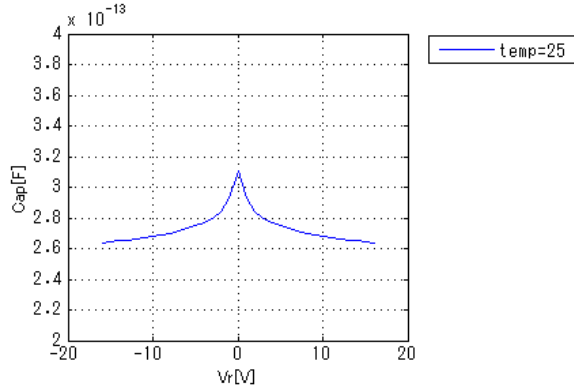
Model Functions Table
RANK=1

Functions	RANK	Implemented
IF-VF(Temp)	1	—
IR-VR(Temp)	1	—
Capacitance	1	○
Reverse recovery characteristics	1	—
Zz-Iz	1	
Rectification characteristics(Bridge)	1	—
Surge-Transient	1	○
tlp	1	○

Simulation results are following.
 Explanatory notes — : simulated

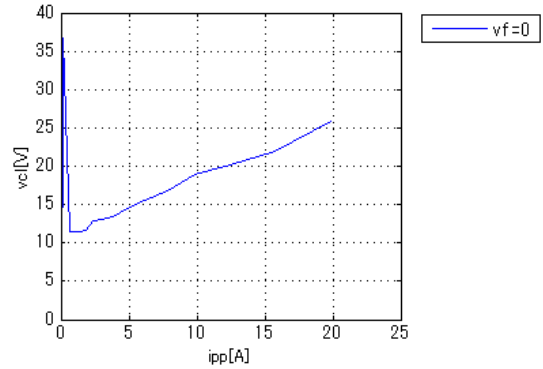
CjVr

Freq = 1000000Hz



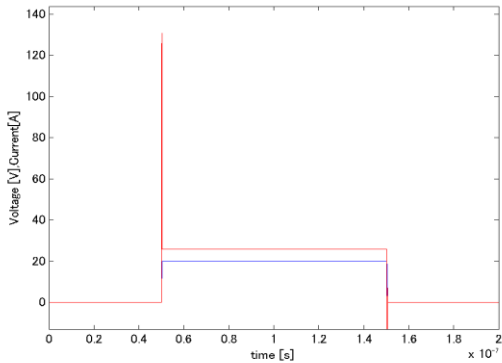
TipVcIpp

tp = 1E-07

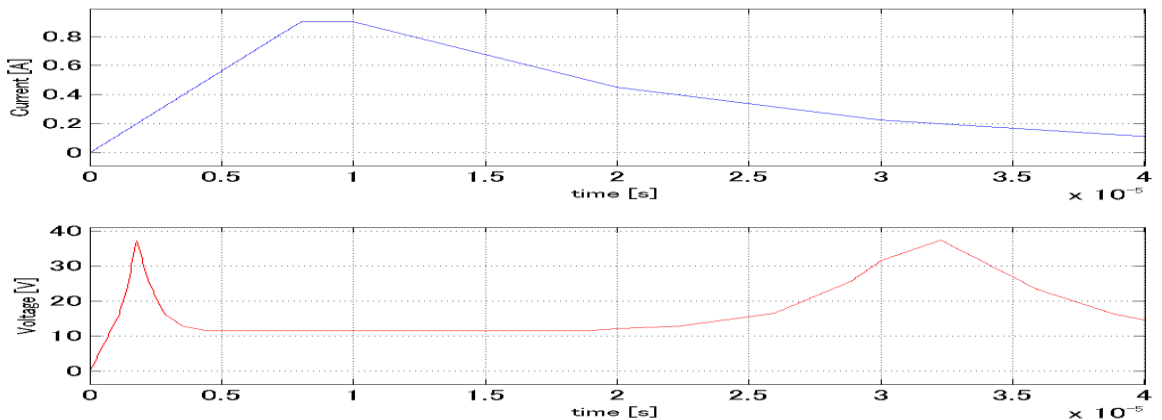


Tip Waveform (Blue : Current Red : Voltage)

tp = 100ns, INPUT = 25.12A outside of the attenuator)

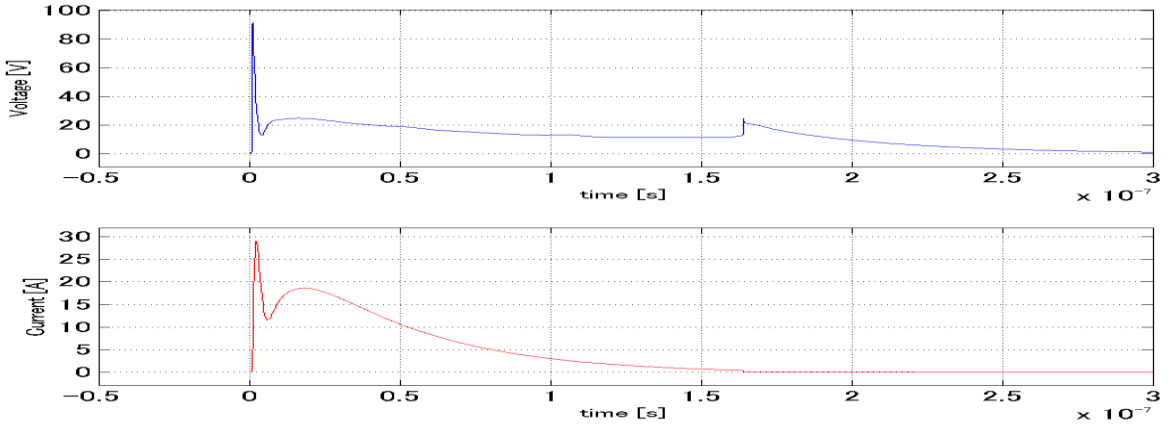


Surge Current Waveform (Reverse 8u/20us)



Simulation results are following.
Explanatory notes — : simulated

Surge Gun Waveform (8kV IEC 61000-4-2)



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