

PSpice Model Photocoupler IC Output TOSHIBA TLP705AF

Model Information

	An original macro model MDC_TLP705AF_PS		
Pin Assign	1:Anode 3:Cathode 4:GND 5:VO 6:VCC		
File List	Model Library	MDC_TLP705AF_PS01.lib	
	Model Report	MDC_TLP705AF_PS.pdf (this file)	

Verified Simulator Version Note

PSpice version 16.6

References

The information which was used for modeling is as follow:

[Data Sheet]	
Date/Version	Rev.5.0
Product name	TLP705AF
Company name	Toshiba Corporation
● Characteristics	IfVf[Temp],IflhTemp[Vcc],IcclTemp[Vcc],IcchTemp[Vcc],Vollo pl[Temp],Vohvccloph[Temp],SwitchingTemp,SwitchingIf,Swit chingVcc

Simulation Range

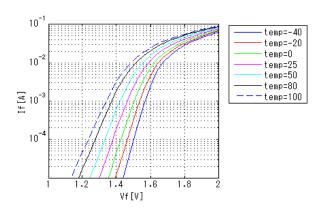
This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

Item	Range			Unit
	Min.		Max.	
Temperature	-55	to	125	deg C



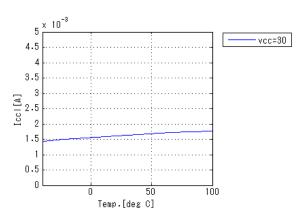
Simulation results are following. Explanatory notes -: simulated

lfVf[Temp]



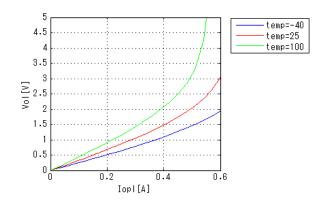
IcclTemp[Vcc]

If = 0A



Vollopl[Temp]

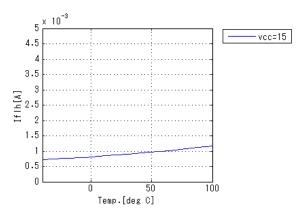
Vcc = 15V, If = 0A



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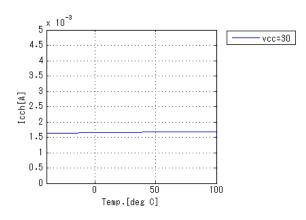
lflhTemp[Vcc]





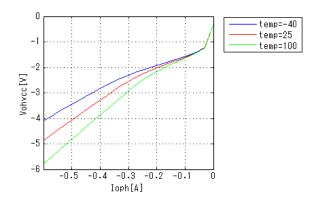
IcchTemp[Vcc]

lf = 0.01A



Vohvccloph[Temp]

Vcc = 15V, If = 0.01A

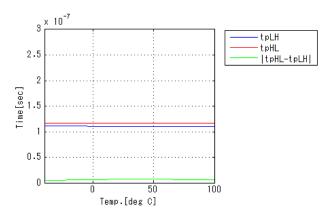




Simulation results are following. Explanatory notes -: simulated

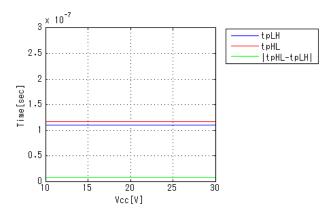
SwitchingTemp

Freq. = 250000Hz, Duty = 50%, If = 0.01A, Vcc = 20V



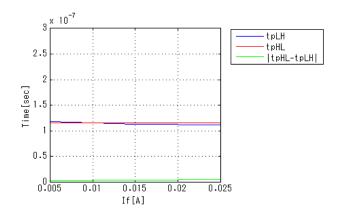
SwitchingVcc

Freq. = 250000Hz, Duty = 50%, If = 0.01A



SwitchingIf

Freq. = 250000Hz, Duty = 50%, Vcc = 20V





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