

MDC_2SC6126_PS

PSpice Model NPN TOSHIBA 2SC6126

Model Information

ModelMextram modelCall NameMDC_2SC6126_PSPin Assign1:C 2:B 3:EFile ListModel Library
Model Report

MDC_2SC6126_PS01.lib MDC_2SC6126_PS.pdf (this file)

Verified Simulator Version Note

PSpice version 16.6

References

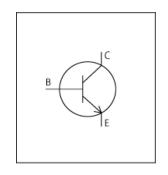
The information which was used for modeling is as follow:

2013-11-01 2SC6126 Toshiba Corporation IcVce[ib],hFEIc[Temp],Vce(sat)Ic[Temp],Vbe(sat)Ic[Temp],Ic Vbe[Temp]

Simulation Range

This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

Item	Range			Unit
	Min.		Max.	
Collector-emitter voltage (DC)	0	to	50	V
Base current (DC)	0	to	2	А
Temperature	-55	to	150	deg C

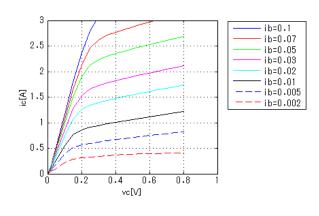




Simulation results are following. Explanatory notes — : simulated

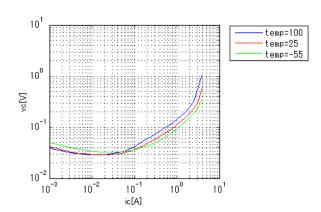
IcVce[ib]

Temp. = 25deg C



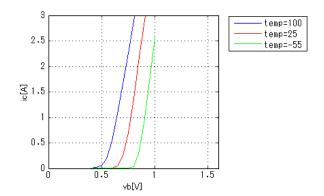
Vce(sat)lc[Temp]

IC/IB = 30



IcVbe[Temp]

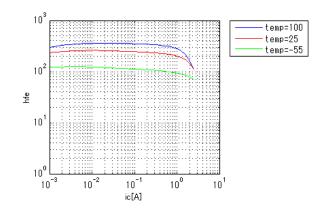
Vce = 2V



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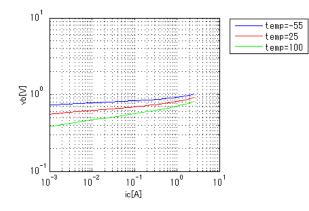
hFElc[Temp]

Vce = 2V



Vbe(sat)lc[Temp]

IC/IB = 30





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MoDeCH Inc.

Head Office Location: Mitsuiseimei Hachioji Bldg., 5-15 Yokoyama-cho, Hachioji-Shi, Tokyo 192-0081, Japan Tel:+81-42-656-3360 E-Mail:model-on-support@modech.co.jp URL:http://www.modech.com/en/