

LTspice Model shunt regulator Texas Instruments TL432AIDBVR

Model Information

Model A macro model

Call Name MDC_TL432AIDBVR_LT

Pin Assign 1:NC 2:ANODE 3:NC 4:CATHODE 5:REF
File List Model Library MDC_TL432AIDBVR_LT.lib

Model Report MDC_TL432AIDBVR_LT.pdf(this file)

Verified Simulator Version LTspice XVII

Note

References

The information which was used for modeling is as follow:

[Data Sheet]

● Date/Version JAJS443Q – AUGUST 2004 – REVISED JULY 2022

Product name
TL432

●Company name Texas Instruments Incorporated

[Characteristics listed]

Characteristics Icathode-VKA

VA-Freq ZKA-Freq Icathde(off) Transient

Simulation Condition

This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

Item	Condition	Unit
Temperature	25	deg C



Model Functions Table

Functions	RANK	Implemented
Icathode-VKA	1	0
VA-Freq	1	0
ZKA-Freq	1	0
Icathde(off)	1	0

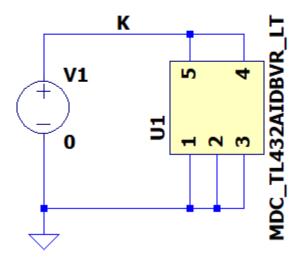
2



Icathode-VKA

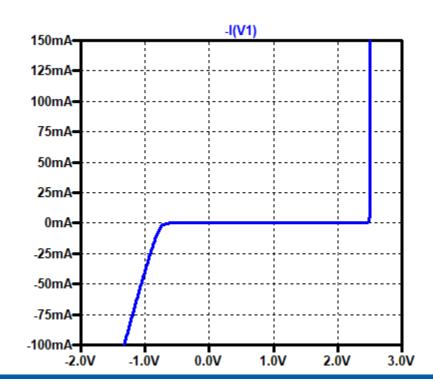
Referred to Data Sheet

.dc V1 -2 3 0.001 .option TNOM=25 .temp 25



Simulation results are following. Explanatory notes — : simulated

Icathode-VKA

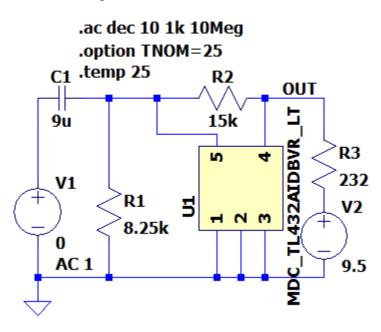




VA-Freq

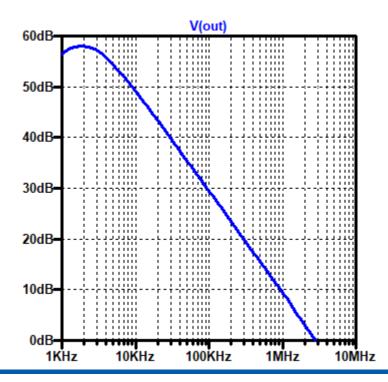
Referred to Data Sheet

.op



Simulation results are following. Explanatory notes — : simulated

VA-Freq

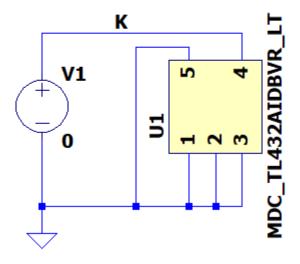




Icathde(off)

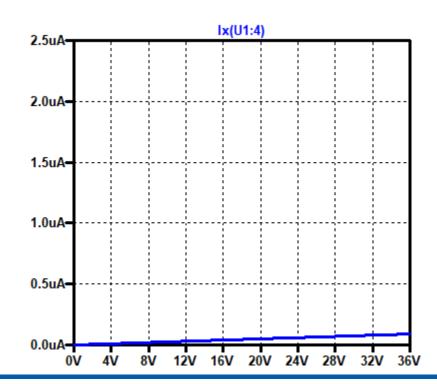
Referred to Data Sheet

.dc V1 0 36 0.1 .option TNOM=25 .temp 25



Simulation results are following. Explanatory notes — : simulated

Icathde(off)



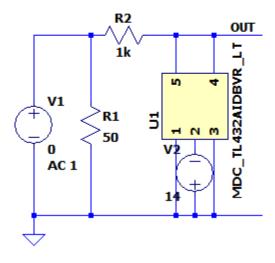


ZKA-Freq

Referred to Data Sheet

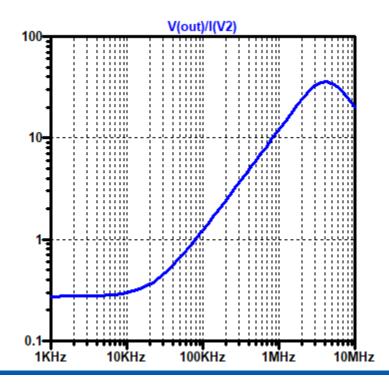
OP.

.ac dec 10 1k 10Meg .option TNOM=25 .temp 25



Simulation results are following. Explanatory notes — : simulated

ZKA-Freq

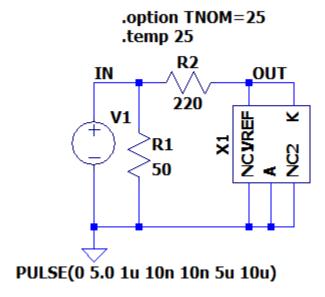




Transient

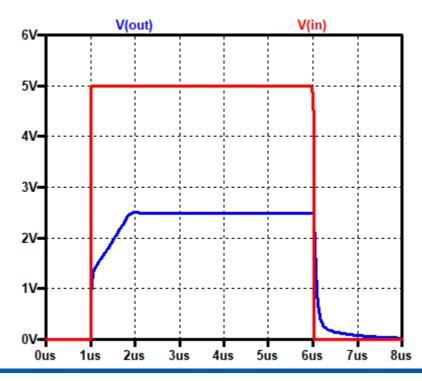
Referred to Data Sheet

.tran 10u



Simulation results are following. Explanatory notes — : simulated

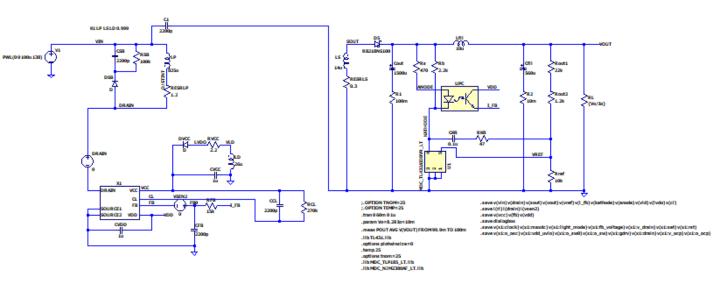
Transient





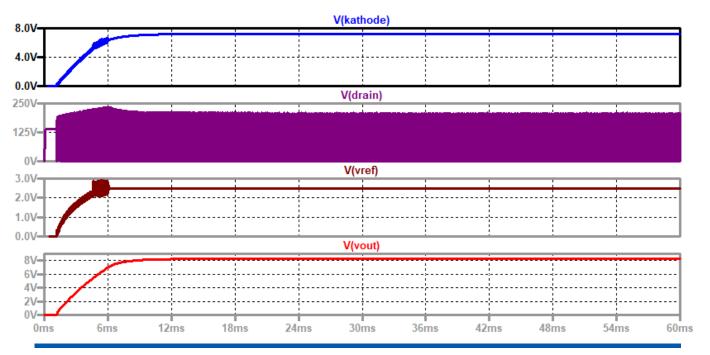
Application:Flyback Converter

Referred to Data Sheet



Simulation results are following. Explanatory notes — : simulated

Application:Flyback Converter



© 2023 MoDeCH inc.



DISCLAIMER

- 1. This SPICE (Simulation Program with Integrated Circuit Emphasis) model and its content (the "Contents") are copyright of MoDeCH Inc. All rights reserved. Any redistribution or reproduction of any or all part of the Contents in any form is prohibited without express written permission made by MoDeCH Inc.
- MoDeCH Inc. as licensor (the "Licensor") hereby grants to you, as licensee (the "Licensee"), a nonexclusive, non-transferable license to use the Contents as long as you abide by the terms and conditions of this DISCLAIMER.
- 3. The Licensee is not authorized to sell, loan, rent and redistribute or license the Contents in whole or in part, or in modified form, to anyone.
- 4. The Licensor shall in no way be liable to the Licensee or any third party for any loss or damage (including ,but not limited to, lost profits, or other incidental, consequential, or punitive damages), however caused (including through negligence) which may be directly or indirectly suffered from, arising out of, or in connection with, any use of the Contents.
- 5. Notwithstanding anything contained in this DISCLAIMER, in no event shall Licensor be liable for any claims, damages or loss which may arise from the modification, combination, operation or use of the Contents with the Licensee's computer programs.
- 6. The Licensor does not warrant that the Contents will function in any environment.
- 7. The Contents may be changed or updated without notice. MoDeCH Inc. may also make improvements and/or changes in the products, pricing and/or the programs related to the Contents at any time without notice.



MoDeCH Inc.

Head Office

Location: 5-15 Yokoyama-cho, Hachioji-Shi, Tokyo 192-0081, Japan

Tel:+81-42-656-3360

E-Mail:model-on-support@modech.co.jp

URL:http://www.modech.com/en/

© 2023 MoDeCH inc.